

## 4 A Simple Compiler

**theory** *Compiler* = *Natural*:

### 4.1 An abstract, simplistic machine

There are only three instructions:

**datatype** *instr* = *ASIN loc aexp* | *JMPF bexp nat* | *JMPB nat*

We describe execution of programs in the machine by an operational (small step) semantics:

**consts** *stepa1* :: *instr list*  $\Rightarrow$   $((\text{state} \times \text{nat}) \times (\text{state} \times \text{nat}))\text{set}$

**syntax**

**@stepa1** :: [*instr list, state, nat, state, nat*]  $\Rightarrow$  *bool*  
     $(\_ \vdash \langle \_, \_ \rangle / -1 \rightarrow \langle \_, \_ \rangle [50, 0, 0, 0, 0] 50)$   
**@stepa** :: [*instr list, state, nat, state, nat*]  $\Rightarrow$  *bool*  
     $(\_ \vdash / \langle \_, \_ \rangle / -* \rightarrow \langle \_, \_ \rangle [50, 0, 0, 0, 0] 50)$

**translations**

$P \vdash \langle s, m \rangle -1 \rightarrow \langle t, n \rangle == ((s, m), t, n) : \text{stepa1 } P$   
 $P \vdash \langle s, m \rangle -* \rightarrow \langle t, n \rangle == ((s, m), t, n) : (\text{stepa1 } P)^*$

**inductive** *stepa1 P*

**intros**

*ASIN[simp]*:

$\llbracket n < \text{size } P; P!n = \text{ASIN } x \ a \rrbracket \Longrightarrow P \vdash \langle s, n \rangle -1 \rightarrow \langle s[x \mapsto a \ s], \text{Suc } n \rangle$

*JMPFT[simp, intro]*:

$\llbracket n < \text{size } P; P!n = \text{JMPF } b \ i; \ b \ s \rrbracket \Longrightarrow P \vdash \langle s, n \rangle -1 \rightarrow \langle s, \text{Suc } n \rangle$

*JMPFF[simp, intro]*:

$\llbracket n < \text{size } P; P!n = \text{JMPF } b \ i; \ \sim b \ s; \ m = n + i \rrbracket \Longrightarrow P \vdash \langle s, n \rangle -1 \rightarrow \langle s, m \rangle$

*JMPB[simp]*:

$\llbracket n < \text{size } P; P!n = \text{JMPB } i; \ i \leq n; \ j = n - i \rrbracket \Longrightarrow P \vdash \langle s, n \rangle -1 \rightarrow \langle s, j \rangle$

### 4.2 The compiler

**consts** *compile* :: *com*  $\Rightarrow$  *instr list*

**primrec**

*compile skip* = []

*compile (x ::= a)* = [*ASIN x a*]

*compile (c1; c2)* = *compile c1* @ *compile c2*

*compile (if b then c1 else c2)* =

    [*JMPF b* (*length(compile c1) + 2*)] @ *compile c1* @

    [*JMPF* (%*x. False*) (*length(compile c2) + 1*)] @ *compile c2*

*compile (while b do c)* = [*JMPF b* (*length(compile c) + 2*)] @ *compile c* @

    [*JMPB* (*length(compile c) + 1*)]

**declare** *nth\_append[simp]*

### 4.3 Context lifting lemmas

Some lemmas for lifting an execution into a prefix and suffix of instructions; only needed for the first proof.

```
lemma app_right_1:
  is1 ⊢ ⟨s1,i1⟩ -1→ ⟨s2,i2⟩ ⇒ is1 @ is2 ⊢ ⟨s1,i1⟩ -1→ ⟨s2,i2⟩
  (is ?P ⇒ _)
proof -
  assume ?P
  then show ?thesis
  by induct force+
qed
```

```
lemma app_left_1:
  is2 ⊢ ⟨s1,i1⟩ -1→ ⟨s2,i2⟩ ⇒
  is1 @ is2 ⊢ ⟨s1,size is1+i1⟩ -1→ ⟨s2,size is1+i2⟩
  (is ?P ⇒ _)
proof -
  assume ?P
  then show ?thesis
  by induct force+
qed
```

```
declare rtrancl_induct2 [induct set: rtrancl]
```

```
lemma app_right:
  is1 ⊢ ⟨s1,i1⟩ -*→ ⟨s2,i2⟩ ⇒ is1 @ is2 ⊢ ⟨s1,i1⟩ -*→ ⟨s2,i2⟩
  (is ?P ⇒ _)
proof -
  assume ?P
  then show ?thesis
  proof induct
    show is1 @ is2 ⊢ ⟨s1,i1⟩ -*→ ⟨s1,i1⟩ by simp
  next
    fix s1' i1' s2 i2
    assume is1 @ is2 ⊢ ⟨s1,i1⟩ -*→ ⟨s1',i1'⟩
      is1 ⊢ ⟨s1',i1'⟩ -1→ ⟨s2,i2⟩
    thus is1 @ is2 ⊢ ⟨s1,i1⟩ -*→ ⟨s2,i2⟩
      by (blast intro: app_right_1 rtrancl_trans)
  qed
qed
```

```
lemma app_left:
  is2 ⊢ ⟨s1,i1⟩ -*→ ⟨s2,i2⟩ ⇒
  is1 @ is2 ⊢ ⟨s1,size is1+i1⟩ -*→ ⟨s2,size is1+i2⟩
  (is ?P ⇒ _)
proof -
  assume ?P
  then show ?thesis
```

```

proof induct
  show  $is1 @ is2 \vdash \langle s1, length\ is1 + i1 \rangle \multimap \langle s1, length\ is1 + i1 \rangle$  by simp
next
  fix  $s1' i1' s2 i2$ 
  assume  $is1 @ is2 \vdash \langle s1, length\ is1 + i1 \rangle \multimap \langle s1', length\ is1 + i1' \rangle$ 
          $is2 \vdash \langle s1', i1' \rangle \multimap \langle s2, i2 \rangle$ 
  thus  $is1 @ is2 \vdash \langle s1, length\ is1 + i1 \rangle \multimap \langle s2, length\ is1 + i2 \rangle$ 
    by (blast intro: app_left_1 rtrancl_trans)
qed
qed

```

```

lemma app_left2:
  [  $is2 \vdash \langle s1, i1 \rangle \multimap \langle s2, i2 \rangle$ ;  $j1 = size\ is1 + i1$ ;  $j2 = size\ is1 + i2$  ]  $\implies$ 
   $is1 @ is2 \vdash \langle s1, j1 \rangle \multimap \langle s2, j2 \rangle$ 
  by (simp add: app_left)

```

```

lemma app1_left:
   $is \vdash \langle s1, i1 \rangle \multimap \langle s2, i2 \rangle \implies$ 
   $instr\ \#\ is \vdash \langle s1, Suc\ i1 \rangle \multimap \langle s2, Suc\ i2 \rangle$ 
  by (erule app_left[of _ _ _ _ [instr], simplified])

```

## 4.4 Compiler correctness

```

declare rtrancl_into_rtrancl[trans]
         rtrancl2_into_rtrancl2[trans]
         rtrancl_trans[trans]

```

The first proof; The statement is very intuitive, but application of induction hypothesis requires the above lifting lemmas

```

theorem  $\langle c, s \rangle \multimap t \implies compile\ c \vdash \langle s, 0 \rangle \multimap \langle t, length(compile\ c) \rangle$ 
         (is ?P  $\implies$  ?Q c s t)

```

```

proof -
  assume ?P
  then show ?thesis
  proof induct
    show  $\bigwedge s. ?Q\ skip\ s\ s$  by simp
  next
    show  $\bigwedge a\ s\ x. ?Q\ (x\ ::=\ a)\ s\ (s[x \mapsto a\ s])$  by force
  next
    fix  $c0\ c1\ s0\ s1\ s2$ 
    assume ?Q  $c0\ s0\ s1$ 
    hence  $compile\ c0 @ compile\ c1 \vdash \langle s0, 0 \rangle \multimap \langle s1, length(compile\ c0) \rangle$ 
      by (rule app_right)
    moreover assume ?Q  $c1\ s1\ s2$ 
    hence  $compile\ c0 @ compile\ c1 \vdash \langle s1, length(compile\ c0) \rangle \multimap$ 
       $\langle s2, length(compile\ c0) + length(compile\ c1) \rangle$ 
  proof -
    note app_left[of _ 0]

```

```

thus
   $\bigwedge is1\ is2\ s1\ s2\ i2.$ 
   $is2 \vdash \langle s1, 0 \rangle \multimap \langle s2, i2 \rangle \implies$ 
   $is1 @ is2 \vdash \langle s1, \text{size } is1 \rangle \multimap \langle s2, \text{size } is1 + i2 \rangle$ 
  by simp

qed
ultimately have compile c0 @ compile c1  $\vdash \langle s0, 0 \rangle \multimap$ 
   $\langle s2, \text{length}(\text{compile } c0) + \text{length}(\text{compile } c1) \rangle$ 
  by (rule rtranc1_trans)
thus ?Q (c0; c1) s0 s2 by simp

next
fix b c0 c1 s0 s1
let ?comp = compile(if b then c0 else c1)
assume b s0 and IH: ?Q c0 s0 s1
hence ?comp  $\vdash \langle s0, 0 \rangle \multimap \langle s0, 1 \rangle$  by auto
also from IH
have ?comp  $\vdash \langle s0, 1 \rangle \multimap \langle s1, \text{length}(\text{compile } c0) + 1 \rangle$ 
  by(auto intro:app1_left app_right)
also have ?comp  $\vdash \langle s1, \text{length}(\text{compile } c0) + 1 \rangle \multimap \langle s1, \text{length } ?comp \rangle$ 
  by(auto)
finally show ?Q (if b then c0 else c1) s0 s1 .

next
fix b c0 c1 s0 s1
let ?comp = compile(if b then c0 else c1)
assume  $\neg b$  s0 and IH: ?Q c1 s0 s1
hence ?comp  $\vdash \langle s0, 0 \rangle \multimap \langle s0, \text{length}(\text{compile } c0) + 2 \rangle$  by auto
also from IH
have ?comp  $\vdash \langle s0, \text{length}(\text{compile } c0) + 2 \rangle \multimap \langle s1, \text{length } ?comp \rangle$ 
  by(force intro!:app_left2 app1_left)
finally show ?Q (if b then c0 else c1) s0 s1 .

next
fix b c and s::state
assume  $\neg b$  s
thus ?Q (while b do c) s s by force

next
fix b c and s0::state and s1 s2
let ?comp = compile(while b do c)
assume b s0 and
  IHc: ?Q c s0 s1 and IHw: ?Q (while b do c) s1 s2
hence ?comp  $\vdash \langle s0, 0 \rangle \multimap \langle s0, 1 \rangle$  by auto
also from IHc
have ?comp  $\vdash \langle s0, 1 \rangle \multimap \langle s1, \text{length}(\text{compile } c) + 1 \rangle$ 
  by(auto intro:app1_left app_right)
also have ?comp  $\vdash \langle s1, \text{length}(\text{compile } c) + 1 \rangle \multimap \langle s1, 0 \rangle$  by simp
also note IHw
finally show ?Q (while b do c) s0 s2.

```

qed  
qed

Second proof; statement is generalized to cater for prefixes and suffixes; needs none of the lifting lemmas, but instantiations of pre/suffix.

```
theorem ⟨c,s⟩ -c→ t ⇒
  !a z. a@compile c@z ⊢ ⟨s,length a⟩ -*→ ⟨t,length a + length(compile c)⟩
apply(erule evalc.induct)
  apply simp
  apply(force intro!: ASIN)
  apply(intro strip)
  apply(erule_tac x = a in allE)
  apply(erule_tac x = a@compile c0 in allE)
  apply(erule_tac x = compile c1@z in allE)
  apply(erule_tac x = z in allE)
  apply(simp add:add_assoc[THEN sym])
  apply(blast intro:rtrancl_trans)

  apply(intro strip)

  apply(erule_tac x = a@[?I] in allE)
  apply(simp)

  apply(rule rtrancl_into_rtrancl2)
  apply(force intro!: JMPFT)

  apply(rule rtrancl_trans)
  apply(erule allE)
  apply assumption

  apply(rule r_into_rtrancl)
  apply(force intro!: JMPFF)

  apply(intro strip)
  apply(erule_tac x = a@[?I]@compile c0@[?J] in allE)
  apply(simp add:add_assoc)
  apply(rule rtrancl_into_rtrancl2)
  apply(force intro!: JMPFF)
  apply(blast)
  apply(force intro: JMPFF)
  apply(intro strip)
  apply(erule_tac x = a@[?I] in allE)
  apply(erule_tac x = a in allE)
  apply(simp)
  apply(rule rtrancl_into_rtrancl2)
  apply(force intro!: JMPFT)
```

```
apply(rule rtranc1_trans)
  apply(erule allE)
  apply assumption
apply(rule rtranc1_into_rtranc12)
  apply(force intro!: JMPB)
apply(simp)
done
```

Missing: the other direction!

```
end
```