

# Semantics of Programming Languages

## Exercise Sheet 6

### Exercise 6.1 A different instruction set architecture

We consider a different instruction set which evaluates boolean expressions on the stack, similar to arithmetic expressions:

- The boolean value *False* is represented by the number 0, the boolean value *True* is represented by any number not equal to 0.
- For every boolean operation exists a corresponding instruction which, similar to arithmetic instructions, operates on values on top of the stack.
- The new instruction set introduces a conditional jump which pops the top-most element from the stack and jumps over a given amount of instructions, if the popped value corresponds to *False*, and otherwise goes to the next instruction.

Modify the theory *Compiler* by defining a suitable set of instructions, by adapting the execution model and the compiler and by updating the correctness proof.

### Exercise 6.2 Deskip

Define a recursive function

**fun** *deskip* :: "*com*  $\Rightarrow$  *com*"

that eliminates as many *SKIP*s as possible from a command. For example:

$deskip (SKIP;; WHILE\ b\ DO\ (x ::= a;; SKIP)) = WHILE\ b\ DO\ x ::= a$

Prove its correctness by induction on *c*:

**lemma** "*deskip* *c*  $\sim$  *c*"

Remember lemma *sim\_while\_cong* for the *WHILE* case.

## Homework 6.1 While Free Programs

Submission until Tuesday, November 25, 10:00am.

- a) Show that while-free programs always terminate, i.e., show that for any while-free command and any state, the big-step semantics yields a result state.
- b) Show that non-terminating programs contain a while loop, i.e., show that all commands, for which there is a state such that the big-step semantics yields no result, contain a while loop.

## Homework 6.2 Absolute Addressing

Submission until Tuesday, November 25, 10:00am.

The current instruction set uses *relative addressing*, i.e., the jump-instructions contain an offset that is added to the program counter. An alternative is *absolute addressing*, where jump-instructions contain the absolute address of the jump target.

Write a semantics that interprets the 3 types of jump instructions with absolute addresses.

**fun** *iexec\_abs* :: “instr  $\Rightarrow$  config  $\Rightarrow$  config”

**definition** *exec1\_abs* :: “instr list  $\Rightarrow$  config  $\Rightarrow$  config  $\Rightarrow$  bool” (“ $\_ / \vdash_a (\_ \rightarrow / \_)$ ” [59,0,59] 60)

**lemma** *exec1\_absI* [intro]:

“ $\llbracket c' = iexec\_abs (P!!i) (i,s,stk); 0 \leq i; i < size P \rrbracket \implies P \vdash_a (i,s,stk) \rightarrow c'$ ”

**abbreviation** *exec\_abs* :: “instr list  $\Rightarrow$  config  $\Rightarrow$  config  $\Rightarrow$  bool” (“ $\_ / \vdash_a (\_ \rightarrow * / \_)$ ” 50)

Write a function that converts a program from absolute to relative addressing:

*cnv\_to\_rel* :: instr list  $\Rightarrow$  instr list

Show that the semantics match wrt. your conversion.

$P \vdash_a c \rightarrow * c' \iff cnv\_to\_rel P \vdash c \rightarrow * c'$

Hints:

- First write a function that converts each instruction, depending on its address. Then use the function *index\_map*, that is defined below, to convert a program.
- Prove the theorem for a single step first.

**fun** *index\_map* :: “(int  $\Rightarrow$  'a  $\Rightarrow$  'a)  $\Rightarrow$  int  $\Rightarrow$  'a list  $\Rightarrow$  'a list”  
— Map with index

**where**

“*index\_map* *f* *i* [] = []”

| “*index\_map* *f* *i* (*x#xs*) = *f* *i* *x* # *index\_map* *f* (*i*+1) *xs*”

Start with proving the following basic facts about *index\_map*, which may be helpful for your main proof!

**lemma** *index\_map\_len*[simp]: “*size (index\_map f i l) = size l*”

— *index\_map* preserves size of list

**lemma** *index\_map\_idx*[simp]: “ $\llbracket 0 \leq i; i < \text{size } l \rrbracket$ ”

$\implies \text{index\_map } f \ k \ l \ !! \ i = f \ (i+k) \ (l!!i)$ ”

— *index\_map* commutes with list indexing

### Homework 6.3 Control Flow Graphs

Submission until Tuesday, November 25, 2014, 10:00am. This homework is worth **5 bonus points**.

From Homework 4.1:

```
type_synonym ('q,'l) lts = "'q  $\Rightarrow$  'l  $\Rightarrow$  'q  $\Rightarrow$  bool"  
inductive word :: "'q,'l) lts  $\Rightarrow$  'q  $\Rightarrow$  'l list  $\Rightarrow$  'q  $\Rightarrow$  bool" for  $\delta$   
where  
  empty: "word  $\delta$  q [] q"  
| prepend: "[ $\delta$  q l qh; word  $\delta$  qh ls q]  $\Longrightarrow$  word  $\delta$  q (l#ls) q"
```

A control flow graph is a labeled transition system (cf. Homework 4.1), where the edges are labeled with actions:

```
datatype action =  
  EAssign vname aexp — Assign variable  
| ETest bexp — Only executable if expression is true  
| ESkip  
type_synonym 'q cfg = "('q,action) lts"
```

Intuitively, the control flow graph is executed by following a path and applying the effects of the actions to the state.

Define the effect of an action to a state. Your function shall return *None* if the action is not executable, i.e., a test of an expression that evaluates to *False*:

```
fun eff :: "action  $\Rightarrow$  state  $\rightarrow$  state" where
```

Lift your definition to paths. Again, only paths where all tests succeed shall yield a result  $\neq$  *None*.

```
fun eff_list :: "action list  $\Rightarrow$  state  $\rightarrow$  state" where
```

The control flow graph of a WHILE-Program can be defined over nodes that are commands. Complete the following definition. (Hint: Have a look at the small-step semantics first)

```
inductive cfg :: "com cfg" where  
  cfg_assign: "cfg (n ::= e) (EAssign n e) (SKIP)"  
| cfg_seq2: "[cfg c1 e c1']  $\Longrightarrow$  cfg (c1;;c2) e (c1';;c2)"
```

Prove that the effects of paths in the CFG match the small-step semantics:

```
lemma eq_path: "(c,s)  $\rightarrow^*$  (c',s')  $\longleftrightarrow$  ( $\exists \pi$ . word cfg c  $\pi$  c'  $\wedge$  eff_list  $\pi$  s = Some s')
```

Hint. Prove the lemma for a single step first.