

# Verified Bytecode Verifiers

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## Abstract

Using the theorem prover Isabelle/HOL we have formalized and proved correct an executable bytecode verifier in the style of Kildall's algorithm for a significant subset of the Java Virtual Machine. First an abstract framework for proving correctness of data flow based type inference algorithms for assembly languages is formalized. It is shown that under certain conditions Kildall's algorithm yields a correct bytecode verifier. Then the framework is instantiated with our previous work about the JVM. Finally we demonstrate the flexibility of the framework by extending our previous JVM model and the executable bytecode verifier with object initialization.

*Key words:* Java, Bytecode Verification, Theorem Proving, Data Flow Analysis, Object Initialization

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## 1 Introduction

Over the past few years there has been considerable interest in formal models for Java and the JVM, and in particular its bytecode verifier (*BCV*). So far most of the work has concentrated on abstract models of particularly tricky aspects of the JVM, specifically object initialization and the idiosyncratic notion of “subroutines”. This paper complements those studies by focussing on machine-checked proofs of executable models. Its distinctive features are:

- The first machine-checked proof of correctness of a *BCV* implementation for a nontrivial subset of the JVM (including some of the tricky aspects mentioned above).

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*URLs:* <http://www.in.tum.de/~kleing> (Gerwin Klein),  
<http://www.in.tum.de/~nipkow> (Tobias Nipkow).

- The BCV is an almost directly executable functional program (which can be generated automatically from its Isabelle/HOL formalization). The few non-executable constructs (some choice functions) are easily implemented.
- The work is modular: the BCV (and its correctness proof!) becomes a simple instance of a general framework for data flow analysis.
- Almost all details of the model are presented. The complete formalization, including proofs, is available via the authors' home page.

Thus the novelty is not in the actual mathematics but in setting up a framework that matches the needs of the JVM, instantiating it with a description of the JVM, and doing this in complete detail, down to an executable program, and including all the proofs.

Moreover this is not an isolated development but it is integrated directly with the existing formalizations of Java and the JVM in Isabelle/HOL [1–3]. From them it also inherits the absence of subroutines and exception handling. Object initialization was not treated in [1–3] either; we will introduce this new feature here, and show how the framework deals with extensions.

Although our only application is the JVM, our modular framework is in principle applicable to other “typed assembly languages” such as MSIL [4] as well; hence the plural in the title.

What does the BCV do and what does it guarantee? The literature already contains a number of (partial) answers to this question. We follow the type system approach of Stata, Abadi and others [5–10]. The type systems check a program w.r.t. additional type annotations that provide the missing typing of storage locations for each instruction. These type systems are then shown to guarantee type soundness, i.e. absence of type errors during execution (which was verified formally by Pusch [3] for a subset of the system by Qian [8]). Only Qian [11] proves the correctness of an algorithm for turning his type checking rules into a data flow analyzer. However, his algorithm is still quite abstract.

Closely related is the work by Goldberg [12] who rephrases and generalizes the overly concrete description of the BCV given in [13] as an instance of a generic data flow framework. Work towards a verified implementation in the SPECWARE system is sketched by Coglio *et al.* [14]. Although we share the lattice-theoretic foundations with this work and it appears to consider roughly the same instruction set, it is otherwise quite different: whereas we solve the data flow problem directly, they generate constraints to be solved separately, which is not described. Furthermore, they state desired properties axiomatically but do not prove them. Casset and Lanet [15] also sketch work using the B method to model the JVM. However, their subset of the JVM is extremely simple (it does not even include classes), and it remains unclear what exactly they have proved. Based on the work of Freund and Mitchell [6] Bertot [16]

has recently used the Coq system to prove the correctness of a bytecode verifier that handles object initialization, but again without classes. Barthe *et al.* [17,18] also use the Coq system; they cover the complete instruction set of the JavaCard platform, but do not handle object initialization.

The rest of the paper is structured as follows. The basic datatypes and semilattices required for data flow analysis are introduced in §2. Our abstract framework relating type systems, data flow analysis, and bytecode verification is set up in §3. It is proved that under certain conditions bytecode verification determines welltypedness. We also show that Kildall’s algorithm, an iterative data flow analysis, is a bytecode verifier. In §4, the results of the previous sections are used to derive a bytecode verifier for the existing  $\mu$ Java formalization. Finally, in §5 we add the object initialization feature to the  $\mu$ Java bytecode verifier, show how to integrate it with Kildall’s algorithm, and discuss its soundness.

## 2 Types, orders, and semilattices

This section introduces the basic mathematical concepts and their formalization in Isabelle/HOL. Note that HOL distinguishes types and sets: types are part of the meta-language and of limited expressiveness, whereas sets are part of the object language and very expressive.

### 2.1 Basic types

Isabelle’s type system is similar to ML’s. There are the basic types *bool*, *nat*, and *int*, and the polymorphic types  $\alpha$  *set* and  $\alpha$  *list* and a conversion function *set* from lists to sets. The “cons” operator on lists is the infix #, concatenation the infix @. The length of a list is denoted by *size*. The *i*-th element (starting with 0) of list *xs* is denoted by *xs* ! *i*. Overwriting the *i*-th element of a list *xs* with a new value *x* is written *xs*[*i* := *x*]. Recursive datatypes are introduced with the *datatype* keyword. The remainder of this section introduces the HOL-formalization of the basic lattice-theoretic concepts required for data flow analysis and its application to the JVM.

### 2.2 Partial orders

Partial orders are formalized as binary predicates. Based on the type synonym  $\alpha$  *ord* =  $\alpha \Rightarrow \alpha \Rightarrow \text{bool}$  and the notations  $x \leq_r y = r\ x\ y$  and

$x <_r y = (x \leq_r y \wedge x \neq y)$  we say that  $r$  is a **partial order** iff the predicate  $order :: \alpha \text{ ord} \Rightarrow bool$  holds for  $r$ :

$$order\ r = (\forall x. x \leq_r x) \wedge (\forall x\ y. x \leq_r y \wedge y \leq_r x \longrightarrow x=y) \wedge \\ (\forall x\ y\ z. x \leq_r y \wedge y \leq_r z \longrightarrow x \leq_r z)$$

We say that  $r$  satisfies the **ascending chain condition** if there is no infinite ascending chain  $x_0 <_r x_1 <_r \dots$  and call  $\top$  a **top element** if  $x \leq_r \top$  for all  $x$ .

### 2.3 Semilattices

Based on the type synonyms  $\alpha\ binop = \alpha \Rightarrow \alpha \Rightarrow \alpha$  and  $\alpha\ sl = \alpha\ set \times \alpha\ ord \times \alpha\ binop$  and the supremum notation  $x +_f y = f\ x\ y$  we say that  $(A, r, f) :: \alpha\ sl$  is a **semilattice** iff the predicate  $semilat :: \alpha\ sl \Rightarrow bool$  holds:

$$semilat\ (A, r, f) = order\ r \wedge closed\ A\ f \wedge \\ (\forall x\ y \in A. x \leq_r x +_f y) \wedge (\forall x\ y \in A. y \leq_r x +_f y) \wedge \\ (\forall x\ y\ z \in A. x \leq_r z \wedge y \leq_r z \longrightarrow x +_f y \leq_r z)$$

where  $closed\ A\ f = \forall x\ y \in A. x +_f y \in A$

Data flow analysis is usually phrased in terms of infimum semilattices. We have chosen a supremum semilattice because it fits better with our intended application, where the ordering is the subtype relation and the join of two types is the least common supertype (if it exists).

We will now look at a few datatypes and the corresponding semilattices which are required for the construction of the JVM bytecode verifier. The definition of those semilattices follows a pattern: we lift an existing semilattice to a new semilattice with more structure. We do this by extending the carrier set, and by giving two functionals  $le$  and  $sup$  that lift the ordering and supremum operation to the new semilattice. In order to avoid name clashes, Isabelle provides separate name spaces for each *theory*, where a theory is like a module in a programming language. Qualified names are of the form *Theoryname.localname*, they apply to constant definitions and functions as well as type constructions. So, if we write *Err.sup* later on, we refer to the  $sup$  functional defined for the error type in §2.4.

### 2.4 The error type and err-semilattices

Theory *Err* introduces an error element to model the situation where the supremum of two elements does not exist. We introduce both a datatype and an equivalent construction on sets:

$datatype \ \alpha \ err = Err \ | \ OK \ \alpha \quad err \ A = \{Err\} \cup \{OK \ a \ | \ a \in A\}$

An ordering  $r$  on  $\alpha$  can be lifted to  $\alpha \ err$  by making  $Err$  the top element:

$$\begin{aligned} le \ r \ (OK \ x) \ (OK \ y) &= x \leq_r \ y \\ le \ r \ \_ \quad \quad \quad Err &= True \\ le \ r \ Err \quad \quad \quad (OK \ y) &= False \end{aligned}$$

We proved that  $le$  preserves the ascending chain condition.

The following lifting functional is frequently useful:

$$\begin{aligned} lift2 \ :: \ (\alpha \Rightarrow \beta \Rightarrow \gamma \ err) \Rightarrow \alpha \ err \Rightarrow \beta \ err \Rightarrow \gamma \ err \\ lift2 \ f \ (OK \ x) \ (OK \ y) &= f \ x \ y \\ lift2 \ f \ \_ \quad \quad \quad \_ &= Err \end{aligned}$$

This brings us to the genuinely new notion of an err-semilattice. It is a variation of a semilattice with top element. Because the behaviour of the ordering and the supremum on the top element are fixed, it suffices to say how they behave on non-top elements. Thus we can represent a semilattice with top element  $Err$  compactly by a triple of type  $esl$ :

$$\alpha \ ebinop = \alpha \Rightarrow \alpha \Rightarrow \alpha \ err \quad \alpha \ esl = \alpha \ set \times \alpha \ ord \times \alpha \ ebinop$$

Conversion between the types  $sl$  and  $esl$  is easy:

$$\begin{aligned} esl \ :: \ \alpha \ sl \Rightarrow \alpha \ esl & \quad \quad \quad sl \ :: \ \alpha \ esl \Rightarrow \alpha \ err \ sl \\ esl(A,r,f) = (A, r, \lambda x \ y. OK(f \ x \ y)) & \quad \quad \quad sl(A,r,f) = (err \ A, le \ r, lift2 \ f) \end{aligned}$$

Now we define  $L \ :: \ \alpha \ esl$  to be an **err-semilattice** iff  $sl \ L$  is a semilattice. It follows easily that  $esl \ L$  is an err-semilattice if  $L$  is a semilattice. The supremum operation of  $sl(esl \ L)$  is useful on its own:

$$sup \ f = lift2 \ (\lambda x \ y. OK(x \ +_f \ y))$$

In a strongly typed environment like HOL we found err-semilattices easier to work with than semilattices with top element.

## 2.5 The option type

Theory *Opt* introduces the new type *option* and the set *opt* as duals to type *err* and set *err*,

$datatype \ \alpha \ option = None \ | \ Some \ \alpha \quad opt \ A = \{None\} \cup \{Some \ a \ | \ a \in A\}$

an ordering that makes *None* the bottom element, and a corresponding supremum operation:

$$\begin{aligned} le\ r\ (Some\ x)\ (Some\ y) &= x \leq_r y \\ le\ r\ None\ \_ &= True \\ le\ r\ (Some\ x)\ None &= False \end{aligned}$$

$$\begin{aligned} sup\ f\ (Some\ x)\ (Some\ y) &= Some(f\ x\ y) \\ sup\ f\ None\ z &= z \\ sup\ f\ z\ None &= z \end{aligned}$$

We proved that function  $sl(A,r,f) = (opt\ A, le\ r, sup\ f)$  maps semilattices to semilattices and that *le* preserves the ascending chain condition.

The *option* datatype is not only useful in the semilattice context, but also serves as a generic way to extend a given datatype by a special element, e.g. to model maps in HOL. A frequently used function is the destructor *the* that satisfies  $the\ (Some\ x) = x$ .

## 2.6 Products

Theory *Product* provides what is known as the *coalesced* product, where the top elements of both components are identified. In terms of err-semilattices, this is

$$\begin{aligned} esl &:: \alpha\ esl \Rightarrow \beta\ esl \Rightarrow (\alpha \times \beta)\ esl \\ esl\ (A,r_A,f_A)\ (B,r_B,f_B) &= (A \times B, le\ r_A\ r_B, sup\ f_A\ f_B) \\ le &:: \alpha\ ord \Rightarrow \beta\ ord \Rightarrow (\alpha \times \beta)\ ord \\ le\ r_A\ r_B &= \lambda(a_1,b_1)(a_2,b_2). a_1 \leq_{r_A} a_2 \wedge b_1 \leq_{r_B} b_2 \\ sup &:: \alpha\ ebinop \Rightarrow \beta\ ebinop \Rightarrow (\alpha \times \beta)\ ebinop \\ sup\ f\ g &= \lambda(a_1,b_1)(a_2,b_2). Err.sup\ (\lambda x\ y.(x,y))\ (a_1 +_f a_2)\ (b_1 +_g b_2) \end{aligned}$$

Note that we use  $\times$  both on the type and set level.

We have shown that if both  $L_1$  and  $L_2$  are err-semilattices, so is  $esl\ L_1\ L_2$ , and that if both  $r_A$  and  $r_B$  satisfy the ascending chain condition, so does  $le\ r_A\ r_B$ .

## 2.7 Lists of fixed length

Theory *Listn* provides the concept of lists of a given length over a given set. In HOL, this is formalized as a set rather than a type:

$$\text{listn } n \ A = \{xs \mid \text{size } xs = n \wedge \text{set } xs \subseteq A\}$$

This set can be turned into a semilattice in a componentwise manner, essentially viewing it as an  $n$ -fold cartesian product:

$$\begin{aligned} sl &:: \text{nat} \Rightarrow \alpha \text{ sl} \Rightarrow \alpha \text{ list } sl & le &:: \alpha \text{ ord} \Rightarrow \alpha \text{ list } ord \\ sl \ n \ (A,r,f) &= (\text{listn } n \ A, \text{le } r, \text{map2 } f) & le \ r &= \text{list-all2 } (\lambda x \ y. x \leq_r y) \end{aligned}$$

where  $\text{map2} :: (\alpha \Rightarrow \beta \Rightarrow \gamma) \Rightarrow \alpha \text{ list} \Rightarrow \beta \text{ list} \Rightarrow \gamma \text{ list}$  and  $\text{list-all2} :: (\alpha \Rightarrow \beta \Rightarrow \text{bool}) \Rightarrow \alpha \text{ list} \Rightarrow \beta \text{ list} \Rightarrow \text{bool}$  are the obvious functions. We introduce the notation  $xs \leq[r] ys = xs \leq_{(le \ r)} ys$ . We have shown (by induction on  $n$ ) that if  $L$  is a semilattice, so is  $sl \ n \ L$ , and that if  $r$  is a partial order and satisfies the ascending chain condition, so does  $le \ r$ .

In case we want to combine lists of different lengths, or if the supremum on the elements of the list may return  $Err$ , we use the following function:

$$\begin{aligned} sup &:: (\alpha \Rightarrow \beta \Rightarrow \gamma \text{ err}) \Rightarrow \alpha \text{ list} \Rightarrow \beta \text{ list} \Rightarrow \gamma \text{ list } err \\ sup \ f \ xs \ ys &= \text{if } \text{size } xs = \text{size } ys \ \text{then } \text{coalesce}(\text{map2 } f \ xs \ ys) \ \text{else } Err \\ \text{coalesce } [] &= OK \\ \text{coalesce } (e\#es) &= Err.\text{sup } (\lambda x \ xs. x\#xs) \ e \ (\text{coalesce } es) \end{aligned}$$

This corresponds to the coalesced product. Below we also need the structure of all lists up to a specific length:

$$\begin{aligned} uptoesl &:: \text{nat} \Rightarrow \alpha \text{ esl} \Rightarrow \alpha \text{ list } esl \\ uptoesl \ n \ (A,r,f) &= (\bigcup_{i \leq n} \text{listn } i \ A, \text{le } r, \text{sup } f) \end{aligned}$$

We have shown that if  $L$  is an err-semilattice, so is  $uptoesl \ n \ L$ .

### 3 Data flow analysis and type system

The purpose of this section is to set up an abstract framework for type checking and data flow analysis of machine code, i.e. lists of instructions. We assume that instructions may be typed (e.g. distinguishing integer from floating point addition) but storage locations are not necessarily typed and may also change their type during execution. Thus it is necessary to infer the type of each storage location at each instruction to see if the instruction will manipulate values of the required type. To keep things abstract, we do not fix the type system or the machine architecture. We simply assume that our model contains a type of **states** that characterizes the state of the machine. This **state type** is a parameter of our setup and will be represented by the type variable  $\sigma$ . Note that  $\sigma$  is intended not to represent values but their abstraction, types. For example, in a register machine  $\sigma$  would be a list of types, one for each register

(roughly speaking). We can now define a **method type**, i.e. the type of a method, simply as a list of state types: each element in the list characterizes the state of the machine before execution of the corresponding instruction. We hasten to add that the name “method” is not significant, we chose it since this is what we will use the framework for later, but the framework itself is not restricted to Java methods. We could also call it program type, or procedure type, or whatever the entities to verify may be. In order to lessen the confusion between types in the programming language under consideration and types in our modeling language, the latter are sometimes referred to as **HOL types**. For example,  $\sigma$  is a HOL type (variable) that represents part of the programming language type system.

Before we continue keeping things as abstract as possible, let us take a look at a concrete example of how things work in the JVM.

The JVM is a stack machine where each method activation has its own expression stack and local variables. The types of operands and results of bytecode instructions are fixed (modulo subtyping), whereas the type of a storage location may differ at different points in the program.

instruction	stack	local variables
<i>Load 0</i>	<i>Some</i> ( $\square$ , [ <i>Class B</i> , <i>Int</i> ] )	
<i>Store 1</i>	<i>Some</i> ( [ <i>Class A</i> ], [ <i>Class B</i> , <i>Err</i> ] )	
<i>Load 0</i>	<i>Some</i> ( $\square$ , [ <i>Class B</i> , <i>Class A</i> ] )	
<i>Getfield F A</i>	<i>Some</i> ( [ <i>Class B</i> ], [ <i>Class B</i> , <i>Class A</i> ] )	
<i>Goto -3</i>	<i>Some</i> ( [ <i>Class A</i> ], [ <i>Class B</i> , <i>Class A</i> ] )	

Fig. 1. Example of a welltyping

In figure 1 on the left the instructions are shown and on the right the type of the stack elements and the local variables. The type information attached to an instruction characterizes the state *before* execution of that instruction. We assume that class *B* is a subclass of *A* and that *A* has a field *F* of type *A*. The *Some* before each of the type entries means that we were able to predict some type for each of the instructions. If one of the instructions had been unreachable, the type entry would have been *None*.

Execution starts with an empty stack and the two local variables hold a reference to an object of class *B* and an integer. The first instruction loads local variable 0, a reference to a *B* object, on the stack. The type information associated with the following instruction may puzzle at first sight: it says that a reference to an *A* object is on the stack, and that usage of local variable 1 may produce an error. This means the type information has become less precise but is still correct: a *B* object is also an *A* object and an integer is now classified as unusable (*Err*). The reason for these more general types is that the predecessor of the *Store* instruction may have either been *Load 0* or

*Goto* – 3. Since there exist different execution paths to reach *Store*, the type information of the two paths has to be “merged”. The type of the second local variable is either *Int* or *Class A*, which are incompatible, i.e. the only common supertype is *Err*.

Bytecode verification is the process of inferring the types on the right from the instruction sequence on the left and some initial condition, and of ensuring that each instruction receives arguments of the correct type. The method type is the right-hand side of the table, a state type is one line of it. Type inference is the computation of a method type from an instruction sequence (usually by iteration), while type checking means checking that a given method type fits an instruction sequence.

With this intuition we turn our attention back to the formalization.

In this abstract setting, we do not yet have to talk about the instruction sequences themselves. They will be hidden inside functions that characterize their behaviour. These functions form the parameters of our model, namely the type system and the data flow analyzer. In the Isabelle formalization, these functions are parameters of everything. In this article, we often make them “implicit parameters”, i.e. we pretend they are global constants, thus increasing readability.

We first introduce a generic framework oriented towards data flow analysis (§3.1). Within it we define and verify a standard data flow analyzer, Kildall’s algorithm (§3.2). Finally we refine the framework towards a static type system (§3.3). In these following three sections let  $(A, r, f)$  be a semilattice.

### 3.1 A generic data flow framework

Data flow analysis and type systems are based on an abstract view of the semantics of a program in terms of types instead of values. Since our programs are sequences of instructions the semantics can be characterized by two functions:

*step* ::  $nat \Rightarrow \sigma \Rightarrow \sigma$  is the abstract execution function: *step*  $p$   $s$  is the result of executing instruction at  $p$  starting in state  $s$ . In the literature *step*  $p$  is called the *transfer function* or *flow function* associated with instruction  $p$ .  
*succs* ::  $nat \Rightarrow nat\ list$  computes the possible successor instructions: *succs*  $p$  returns  $[q_1, \dots, q_k]$  iff execution of instruction  $p$  may transfer control to any of the instructions  $q_1, \dots, q_k$ . We use lists instead of sets for reasons of executability.

We say that *succs* is **bounded by**  $n$  iff for all  $p < n$  the elements of *succs*  $p$  are less than  $n$ . This expresses that from below instruction  $n$ , instruction  $n$  and beyond are unreachable, i.e. control never leaves the list of instructions below  $n$ .

Data flow analysis is concerned with solving data flow equations, i.e. systems of equations involving the flow functions over a semilattice. In our case *step* is the flow function and  $\sigma$  the semilattice. Instead of an explicit formalization of the data flow equation it suffices to consider certain prefixed points. To that end we define what it means that a method type *ss* is **stable at**  $p$  and **stable**:

$$\text{stable } ss \ p \equiv \forall q \in \text{set}(\text{succs } p). \text{ step } p \ (ss!p) \leq_r \ ss!q$$

$$\text{stables } ss \equiv \forall p < \text{size } ss. \text{ stable } ss \ p$$

The stable method types are the prefixed points of the associated data flow equation for *step*

$$ss!p = \sum_{q \in \text{succs}^{-1} p} \text{step } q \ (ss!q)$$

where we assume that  $r$  is a semilattice and  $\sum$  the corresponding supremum operation. Note that this equation has only been included here for clarity and is not part of our formalization. Our reference point will be the notion of stability. It induces the notion of a method type *ts* being a **welltyping w.r.t. step**:

$$\text{wt-step } ts \equiv \forall p < \text{size } ts. \text{ ts!p} \neq \top \wedge \text{ stable } ts \ p$$

Here  $\top$ , another implicit parameter of *wt-step*, is assumed to be a special element in the state space indicating a type error. Usually  $\top$  will be the top element of the ordering.

A welltyping is a witness of welltypedness in the sense of stability. Now we turn to the problem of computing such a witness. This is precisely the task of a bytecode verifier: it computes a method type such that the absence of  $\top$  in the result means the method is welltyped. Formally, a function  $bcv :: \sigma \text{ list} \Rightarrow \sigma \text{ list}$  is a **bytecode verifier** (w.r.t.  $n :: \text{nat}$  and  $A :: \sigma \text{ set}$ , see below) iff

$$\forall ss \in \text{list } n \ A. (\forall p < n. (bcv \ ss)!p \neq \top) = (\exists ts \in \text{list } n \ A. ss \leq[r] \ ts \wedge \text{wt-step } ts)$$

The notation  $\leq[r]$  lifts  $\leq_r$  to lists (see §2.7). In practice,  $bcv \ ss$  itself will be the welltyping, and it will also be the least welltyping. However, it is simpler not to require this.

The introduction of a subset  $A$  of the state space  $\sigma$  is necessary to make distinctions beyond HOL's type system: for example, when representing a list of registers,  $\sigma$  is likely to be a HOL list type; but the fact that in any particular

program the number of registers is fixed cannot be expressed as a HOL type, because it requires dependent types to formalize lists of a fixed length. We use sets to express such fine grained distinctions.

### 3.2 Kildall's algorithm

This section first defines and then verifies a functional version of Kildall's algorithm [19,20], a standard data flow analysis tool. In fact, the description of bytecode verification in the official JVM specification [13, pages 129–130] is essentially Kildall's algorithm, an iterative computation of the solution to a data flow problem. The main loop operates on a method type  $ss$  and a **worklist**  $w :: nat\ set$ . The worklist contains the indices of those elements of  $ss$  that have changed and whose changes still need to be propagated to their successors. Each iteration picks an element  $p$  from  $w$ , executes instruction number  $p$ , and propagates the new state to the successor instructions of  $p$ . Iteration terminates once  $w$  becomes empty: in each iteration,  $p$  is removed but new elements can be added to  $w$ . For reasons detailed below, the algorithm is expressed in terms of a predefined *while*-combinator of type  $(\alpha \Rightarrow bool) \Rightarrow (\alpha \Rightarrow \alpha) \Rightarrow \alpha \Rightarrow \alpha$  which satisfies the recursion equation

$$while\ b\ c\ s = (if\ b\ s\ then\ while\ b\ c\ (c\ s)\ else\ s)$$

Clearly  $while\ (\lambda s. b\ s)\ (\lambda s. c\ s)$  is the functional counterpart of the imperative program `while b(s) do s := c(s)`. The main loop can now be expressed as

$$\begin{aligned} iter\ ss\ w = & while\ (\lambda(ss,w). w \neq \{\}) \\ & (\lambda(ss,w). let\ p = SOME\ p. p \in w; t = step\ p\ (ss!p) \\ & \quad in\ propa\ (succs\ p)\ t\ ss\ (w - \{p\})) \\ & (ss,w) \end{aligned}$$

As in §3.1 the functions *step* and *succs* are implicit parameters.

The choice of which position to consider next is made by Hilbert's  $\varepsilon$ -operator, written *SOME* above: *SOME*  $x. P\ x$  is some arbitrary but fixed  $x$  such that  $P\ x$  holds; if there is no such  $x$ , then the value of *SOME*  $x. P\ x$  is arbitrary but still defined. Since the choice in *iter* is guarded by  $w \neq \{\}$ , we know that  $p \in w$ . An implementation is free to choose whichever element it wants.

Propagating the result  $t$  of executing instruction number  $p$  to all successors is expressed by the primitive recursive function *propa*:

$$\begin{aligned} propa\ []\ t\ ss\ w = & (ss,w) \\ propa\ (q\#\!qs)\ t\ ss\ w = & (let\ u = t +_f\ ss!q; \\ & \quad w' = (if\ u = ss!q\ then\ w\ else\ \{q\} \cup w) \\ & \quad in\ propa\ qs\ t\ (ss[q := u])\ w') \end{aligned}$$

In the terminology of the official JVM specification [13, page 130]  $t$  is “merged” with the state of all successor instructions  $q$ , i.e. the supremum is computed. If this results in a change of  $ss \vdash q$  then  $q$  is inserted into  $w$ . The supremum operation  $f$  is another implicit parameter of the algorithm.

Kildall’s algorithm is simply a call to  $iter$  where the worklist is initialized with the set of unstable indices; upon termination we project on the first ( $fst$ ) component:

$$kildall\ ss = fst(iter\ ss\ \{p. p < size\ ss \wedge \neg stable\ ss\ p\})$$

Essentially the same algorithm was presented in [21], with one important difference: we did not show the actual definition of  $iter$ , because that was an opaque well-founded recursion. The recursion equation shown instead was what one would have written in a programming language with partial functions, but in HOL this is impossible, because  $iter$  is partial. It is only now that the *while*-combinator has become available that we can really define  $iter$  in a natural and directly executable form. The definition of *while* itself and the derivation of the corresponding recursion equation is not completely trivial; for details see [22].

The key theorem is that Kildall’s algorithm is a bytecode verifier as defined above:

**Theorem 1** *If  $(A, r, f)$  is a semilattice,  $r$  meets the ascending chain condition,  $step$  is monotone (w.r.t.  $A$  and  $n$ ) and preserves  $A$  upto  $n$ , and  $succs$  is bounded by  $n$ , then  $kildall$  is a bytecode verifier.*

This theorem is a corollary to the following lemma about  $iter$ :

**Lemma 2** *Assuming the same preconditions as in Theorem 1, if  $ss_0$  is stable at all  $p < n$  that are not in  $w_0$ ,  $ss_0 \in list\ n\ A$ ,  $w_0$  is bounded in size by  $n$ , and  $iter\ ss_0\ w_0 = (ss', w')$ , then*

$$\begin{aligned} &ss' \in list\ n\ A \wedge stables\ ss' \wedge ss_0 \leq[r]\ ss' \wedge \\ &(\forall ts \in list\ n\ A. ss_0 \leq[r]\ ts \wedge stables\ ts \longrightarrow ss' \leq[r]\ ts) \end{aligned}$$

This lemma is proved with the help of the usual proof rule for *while*-loops rephrased functionally in terms of the *while*-combinator. That theorem is already part of the Isabelle library. The invariant is the following predicate on  $ss$  and  $w$ :

$$\begin{aligned} &ss \in list\ n\ A \wedge (\forall p < n. p \notin w \longrightarrow stable\ ss\ p) \wedge ss_0 \leq[r]\ ss \wedge \\ &(\forall ts \in list\ n\ A. ss_0 \leq[r]\ ts \wedge stables\ ts \longrightarrow ss \leq[r]\ ts) \wedge (\forall p \in w. p < n) \end{aligned}$$

Except for the last conjunct, this is identical to the desired postcondition. Hence it is easy to see that upon termination ( $w = \{\}$ ) the postcondition

holds. Similarly, it is easy to see that the invariant holds initially, i.e. for  $ss = ss_0$  and  $w = w_0$ . Preservation of the invariant requires a number of lemmas about *propa* which are not interesting enough to warrant showing them here. Termination is proved with the help of the lexicographic product of two well-founded orderings: with every iteration, either  $ss$  increases w.r.t.  $\leq[r]$  (hence we need the ascending chain condition), or  $ss$  stays unchanged but  $w$  decreases (and  $w$  is finite because it is bounded by  $n$ ).

### 3.3 Refining step

We close this section with a little refinement of the generic *step*. In practice, *step* will be composed of two functions: one that checks the applicability of the instruction in the current state, and one that carries out the instruction assuming it is applicable. These two functions will be called *app* and *eff*. Furthermore the state space  $\sigma$  will be of the form  $\tau \text{ err}$  for a suitable type  $\tau$ . In which case the error element  $\top$  above is *Err* itself. Given functions  $app :: nat \Rightarrow \tau \Rightarrow bool$  and  $eff :: nat \Rightarrow \tau \Rightarrow \tau$ , *step* is easily defined:

$$step\ p \equiv lift\ (\lambda t. \text{if } app\ p\ t \text{ then } OK(eff\ p\ t) \text{ else } Err)$$

Similarly, we can refine the notion of welltyping w.r.t. *step* to welltyping w.r.t. *app* and *eff*:

$$wt\text{-}app\text{-}eff\ ts \equiv \forall p < size\ ts. app\ p\ (ts!p) \wedge (\forall q \in set(succs\ p). eff\ p\ (ts!p) \leq_r ts!q)$$

This is very natural: every instruction is applicable in its start state, and the effect is compatible with the state expected by all successor instructions.

It is almost but not quite the case that *wt-step* and *wt-app-eff* coincide. We have proved the following two lemmas:

**Lemma 3** *If succs is bounded by size ts and every instruction has a successor, then wt-step ts implies wt-app-eff (map ok-val ts) where ok-val (OK x) = x.*

Why the requirement that every instruction must have a successor? Otherwise *ts* may be a welltyping w.r.t. *step* for the trivial reason that *stable ts p* is vacuously true if  $succs\ p = \{\}$ , even though  $app\ p\ (ts!p)$  may be false. In the other direction, this problem disappears:

**Lemma 4** *If succs is bounded by size ts, then wt-app-eff ts implies that wt-step (map OK ts).*

In a sense *wt-app-eff* is more precise than *wt-step*. This is not surprising: it has more structure to work with. We can still model everything that is captured by *wt-app-eff* with *wt-step*, though: lemma 3 tells us that they agree when

each instruction has an successor, so for instructions with no successor we just define  $\text{succs } p = \{p\}$  and  $\text{eff } p = \text{id}$ . This does not change the behaviour of the BCV and makes sure that both formulations are equivalent.

## 4 JVM

In the sections above we have presented an abstract definition of welltyping and also a means to calculate one. We will now instantiate these abstract notions for the  $\mu\text{Java}$  Virtual Machine. The instantiation presented here is a bit different from the one we already gave in [21]. The version we will discuss here is a direct integration with our existing  $\mu\text{Java}$  formalization. The theory in [21] was based on  $\mu\text{Java}$  in that it used the same concepts, instruction set etc, but formally it was a separate theory. We will first give an overview of the  $\mu\text{Java}$  formalization—see [2] for an in depth introduction to a slightly earlier version of it—and then discuss how it works together with the definitions of the bytecode verifier framework.

The  $\mu\text{Java}$  model is a downsized version of the real Java language and JVM. It does not contain threads, interfaces, packages, or visibility modifiers like `public` or `private`. It does contain a small but representative instruction set, classes, inheritance, and object oriented method invocation. The formalization includes the source language, its operational semantics and type system together with a proof of type safety, and also the bytecode language together with its operational semantics, type system, proof of type safety, and now also an executable verified bytecode verifier. To keep things abstract and manageable, the source and bytecode language share as many notions as possible, i.e. program structure, wellformedness of programs, large parts of the type system, etc.

### 4.1 The $\mu\text{Java}$ VM

Since source and target languages share the same model of program structure, the virtual machine of  $\mu\text{Java}$  does not have a notion of class files and constant pool like Sun's JVM has. A program is a list of classes and their declaration information, a class a list of methods and fields. For bytecode verification we will only be concerned with the method level, so we only describe the types at this level in more detail:

$$\begin{aligned} \gamma \text{ mdecl} &= \text{sig} \times \text{ty} \times \gamma \\ \text{sig} &= \text{mname} \times \text{ty list} \end{aligned}$$

Since they are used for both the source and the bytecode level,  $\mu\text{Java}$  method

declarations are parameterized: they consist of a signature (name and parameter types), the return type ( $ty$  are the Java types), and a method body of type  $\gamma$  (which in our case will be the bytecode).

Method declarations come with a method dictionary lookup function  $method$ , such that  $method(\Gamma, C) sig$  looks up a method with signature  $sig$  in class  $C$  of program  $\Gamma$ . It yields a value of type  $(cname \times ty \times \gamma) option$  indicating whether a method with that signature exists, in which class it is defined (it could be a superclass of  $C$  since  $method$  takes inheritance and overriding into account), and also the rest of the declaration information: the return type and body.

The runtime environment, i.e. the state space of the  $\mu$ JVM, is modeled a bit more closely after the real thing. The state consists of a heap, a stack of call frames, and a flag whether an exception was raised (and if yes, which one).

The heap is simple: a partial function from locations to objects, in Isabelle  $aheap = loc \Rightarrow obj option$ . Here,  $loc$  is the type of addresses and  $obj$  is short for  $cname \times (vname \times cname \Rightarrow val option)$ , i.e. each object is a pair consisting of a class name (the class the object belongs to) and a mapping for the fields of the object (taking the name and defining class of a field, and yielding its value if such a field exists,  $None$  otherwise).

In the  $\mu$ JVM each method execution gets its own call frame, containing its own operand stack (a list of values), its own set of local variables (also a list of values), and its own program counter. Since we later need to know which method each call frame belongs to, we also store the class and signature (i.e. name and parameter types) of the method and arrive at:

$$\begin{aligned} frame &= opstack \times locvars \times cname \times sig \times nat \\ opstack &= val list \\ locvars &= val list \end{aligned}$$

The  $\mu$ JVM state is then:

$$\begin{aligned} jvm\text{-}state &= xcpt option \times aheap \times frame list \\ xcpt &= NullPointerException \mid ClassCast \mid OutOfMemory \end{aligned}$$

## 4.2 Operational semantics

This section sketches the state transition relation of the  $\mu$ Java VM. We will be relatively brief here and only concentrate on the parts we need for the BCV.

Figure 2 shows the instruction set. Method bodies are lists of such instructions together with two integers  $maxs$  and  $maxl$  containing the maximum operand

stack size and the number of local variables (not counting the *this* pointer and parameters of the method which get stored in the first  $0$  to  $n$  registers). So the type parameter  $\gamma$  for method bodies gets instantiated with  $\text{nat} \times \text{nat} \times \text{instr list}$ , i.e.  $mdecl = sig \times ty \times \text{nat} \times \text{nat} \times \text{instr list}$ .

<i>datatype instr =</i>	
<i>Load nat</i>	load from local variable
<i>Store nat</i>	store into local variable
<i>LitPush val</i>	push a literal (constant)
<i>New cname</i>	create object on heap
<i>Getfield vname cname</i>	fetch field from object
<i>Putfield vname cname</i>	set field in object
<i>Checkcast cname</i>	check if object is of given type
<i>Invoke cname mname (ty list)</i>	invoke instance method
<i>Return</i>	return from method
<i>Dup</i>	duplicate top element
<i>IAdd</i>	integer addition
<i>Goto int</i>	goto relative address
<i>Ifcmpeq int</i>	branch if top elements are equal

Fig. 2. The  $\mu$ Java instruction set

The state transition relation  $s \xrightarrow{\text{jvm}} t$  is built on a function *exec* describing one-step execution:

```

exec :: jvm-state  $\Rightarrow$  jvm-state option
exec (xp, hp, []) = None
exec (Some xp, hp, frs) = None
exec (None, hp, f#frs) = let (stk,loc,C,sig,pc) = f;
                               i = (5th (the (method ( $\Gamma,C$ ) sig))) ! pc
                               in Some (exec-instr i hp stk loc C sig pc frs)

```

It says that execution halts if the call frame stack is empty or an exception has occurred. In all other cases execution is defined, *exec* decomposes the top call frame, looks up the current method, retrieves the instruction list (the 5th element) of that method, and delegates the rest to *exec-instr* which defines the execution rules for single instructions. As throughout the rest of this article, the program  $\Gamma$  is treated as global parameter.

The state transition relation is the reflexive transitive closure of the defined part of *exec*:

$$s \xrightarrow{\text{jvm}} t = (s,t) \in \{(s,t) \mid \text{exec } s = \text{Some } t\}^*$$

The definition of *exec-instr* is straightforward for most instructions. In the *Load idx* case for instance, we just take the value at position *idx* in the local variables and put it on top of the stack. Apart from incrementing the program counter the rest remains untouched:

```

exec-instr (Load idx) hp stk vars Cl sig pc frs =
  (None, hp, ((vars ! idx) # stk, vars, Cl, sig, pc+1)#frs)

```

Since they will recur in a slightly different form in §5.2 we also show the rules for *New*, *Invoke* and *Return*:

```

exec-instr (New C) hp stk vars Cl sig pc frs =
let (ref, xp') = new-Addr hp;
    hp' = if xp'=None then hp(ref := Some (blank C)) else hp;
    stk' = if xp'=None then (Addr ref)#stk else stk
in (xp', hp', (stk', vars, Cl, sig, pc+1)#frs)

```

The *New* rule uses two helper functions  $new-Addr :: aheap \Rightarrow loc \times xcpt\ option$  and  $blank :: cname \Rightarrow obj$ . The former returns a new, unused address on the heap or an *OutOfMemory* exception. The latter constructs a blank object with all fields set to their default values. With these, the *New C* instruction either sets the exception flag to *OutOfMemory* or constructs a new object on the heap and puts its address on top of the stack.

*Invoke* on a method with name  $mn$  of class  $C$  with formal parameter types  $ps$  is a bit more involved:

```

exec-instr (Invoke C mn ps) hp stk vars Cl sig pc frs =
let
  n = size ps;
  args = take n stk;
  ref = stk!n;
  xp' = raise-xcpt (ref=None) NullPointer;
  D = fst(the(hp(the-Addr ref)));
  (dc, -, -, mxl, _) = the (method (Γ,D) (mn,ps));
  frs' = if xp'≠None then [] else
    [([],ref#rev args@replicate mxl arbitrary,dc,(mn,ps),0)]
in (xp', hp, frs'@(stk, vars, Cl, sig, pc)#frs)

```

The first  $n$  values on the stack are the actual parameters, the next value after that is the reference on which to invoke the method. If it is *Null*, the flag is set to *NullPointer* exception and execution will stop. Otherwise, we look up the dynamic type of the object (which could be a subclass of  $C$ ) on the heap, and do a method lookup with this dynamic type to find out in which class  $dc$  the method really is defined.

With these values we construct a new call frame for the new method: initially, it has an empty stack, a reference to *this* in local variable  $0$ , the actual parameters in variables  $1$  to  $n$ , and the rest of the local variables filled with an arbitrary value. We fill in the defining class  $dc$ , the signature of the method, and set the  $pc$  to instruction  $0$ . The new call frame is put in front of the current one which remains unchanged until the newly invoked method returns.

If execution of the new method ends, it must end with a *Return* instruction:

```

exec-instr Return G hp stk0 vars Cl sig0 pc frs =
  if frs=[] then (None, hp, []) else
  let val = hd stk0;
      (stk,loc,C,sig,pc) = hd frs;
      n = length (snd sig0)
  in (None, hp, (val#(drop (n+1) stk),loc,C,sig,pc+1)#tl frs)

```

*Return* does nothing if the call frame is already empty. If it is not, the return value is the top of stack in the current call frame, the current call frame is removed and the frame directly beneath (the calling method) is updated: we drop the parameters and object reference of the *Invoke* instruction from the stack, push the return value, and increment the *pc*.

These rules obviously do not implement a defensive VM. Nowhere do we check if values have the right type, if the operand stack contains values at all when we refer to its top, if a local variable with a given index even exists, etc. Since HOL is a logic of total functions, execution in all these cases is still defined. However, we do not know anything about such defined but unspecified states, and we will certainly not be able to prove any interesting properties like type conformance about them.

### 4.3 Bytecode verification and type safety

This section presents the BCV specification for  $\mu$ Java, and its integration with the executable BCV framework of §3.2.

We will proceed as follows: first, we again take a look at the state space—this time with types instead of values since we are interested in abstract execution only. Then, we will turn this state space into a semilattice which gives us an ordering on it and a supremum operation. We will use the ordering to describe welltypings statically. This part is taken from the existing  $\mu$ Java formalization, it comes with a type safety result about welltypings. The supremum operation is the part we additionally need for Kildall’s algorithm. We will see that the result of Kildall’s algorithm agrees with the specification of welltypings. This finally implies that the type safety results for  $\mu$ Java welltypings also apply to the executable BCV.

The  $\mu$ Java BCV specification describes how welltypings of operand stack and local variables of a method look like (as in the example figure 1 at the very beginning). Values on the operand stack must always contain a known type, the type of values in the local variables may be unknown (encoded by *Err*):

$state\text{-}type = ty\ list \times ty\ err\ list$

$datatype\ ty = PrimT\ prim\text{-}ty \mid RefT\ ref\text{-}ty$

$datatype\ prim\text{-}ty = Void \mid Bool \mid Int$

$datatype\ ref\text{-}ty = NullT \mid ClassT\ cname$

In this definition,  $ty$  is the HOL type describing the  $\mu$ Java type system. It consists of primitive types like  $Int$  or  $Void$ , and references types. We abbreviate the reference type  $RefT\ (ClassT\ C)$  by  $Class\ C$  and  $RefT\ NullT$ , the type of the value  $Null$ , by  $NT$ .

The  $\mu$ Java types come with a subtype ordering  $\preceq$  that builds on the direct subclass relation  $subcls\ \Gamma$  induced by the program  $\Gamma$ . It satisfies:

$$\begin{array}{lcl} T & \preceq & T \\ NT & \preceq & RefT\ T \\ Class\ C & \preceq & Class\ D \quad \text{if } (C,D) \in (subcls\ \Gamma)^* \end{array}$$

where  $(C,D) \in (subcls\ \Gamma)^*$  means that  $C$  is a subclass of  $D$ .

For every class hierarchy, that is for every program, this subtype ordering may be a different one. In the Isabelle formalization the ordering  $\preceq$  therefore has  $\Gamma$  as an additional parameter, in this paper  $\Gamma$  is an implicit parameter of everything.

To handle unreachable code, the BCV will not directly work on  $state\text{-}type$ , but on  $state\text{-}type\ option$  instead. In the dataflow analysis  $None$  will indicate the state type of instructions that have not been reached yet. If  $None$  occurs in the welltyping, the corresponding instruction is unreachable.

#### 4.3.1 Java Types as Semilattice

If we want to instantiate Kildall's algorithm we need to turn first  $ty$  and then  $state\text{-}type$  into a semilattice.

Theory  $JType$  concerns the semilattice structure of  $ty$ : the ordering we already have with  $\preceq$ . For the carrier set  $types$  we take the set of all primitive types and all subclasses of  $Object$  defined in the program. The supremum operation  $sup$  is also relatively straightforward:

$$\begin{array}{lcl} sup :: ty \Rightarrow ty \Rightarrow ty\ err \\ sup\ NT & (Class\ C) = & OK\ (Class\ C) \\ sup\ (Class\ C)\ NT & = & OK\ (Class\ C) \\ sup\ (Class\ C)\ (Class\ D) & = & OK\ (Class\ (some\text{-}lub\ C\ D)) \\ sup\ t_1 & t_2 & =\ if\ t_1 = t_2\ then\ OK\ t_1\ else\ Err \end{array}$$

The auxiliary function *some-lub* used in the computation of the supremum of two classes is defined non-constructively (as some least upper bound, using Hilbert’s  $\varepsilon$ -operator). Of course we also prove that (under suitable conditions) least upper bounds are uniquely determined and exist. Thus our work is independent of the particular algorithm used for this calculation.

**Theorem 5** *The triple  $esl \equiv (types, \preceq, sup)$  is an err-semilattice provided the class hierarchy  $subcls \Gamma$  is **single valued** (each subclass has at most one direct superclass, i.e.  $\Gamma$  represents a single inheritance hierarchy) and  $subcls \Gamma$  is acyclic.*

Univalence and acyclicity together imply that  $subcls \Gamma$  is a set of trees, and *types* focuses on the subtree below *Object*.

Because any infinite subtype chain would induce an infinite subclass chain we also obtain

**Lemma 6** *If  $(subcls \Gamma)^{-1}$  is wellfounded (there is no infinite ascending subclass chain  $(C_i, C_{i+1}) \in subcls \Gamma$ ) then  $\preceq$  satisfies the ascending chain condition.*

We call  $\Gamma$  **wellformed** if  $subcls \Gamma$  is single valued and  $(subcls \Gamma)^{-1}$  is wellfounded.

Note that by incorporating a fixed class hierarchy into our model we assume that all required classes have been loaded, i.e. we model an eager class loader. Although Sun’s JVM is a bit lazier, the JVM specification [13, p. 127] does permit eager loading.

Now we come to the core of the type system, namely the semilattice structure  $\tau$  on *state-type option*. Turning *state-type option* into a semilattice is easy, because all of its constituent types are (err-)semilattices. The expression stacks form an err-semilattice because the supremum of stacks of different size is *Err*; the list of registers forms a semilattice because the number *mxr* of registers is fixed (it is the number *mxl* of local variables plus the number of parameters of the method plus one for the *this* pointer):

$$\begin{aligned} stk-esl &:: nat \Rightarrow ty \ list \ esl \\ stk-esl \ mxs &\equiv upto-esl \ mxs \ (JType.esl) \end{aligned}$$

$$\begin{aligned} reg-sl &:: nat \Rightarrow ty \ err \ list \ sl \\ reg-sl \ mxr &\equiv Listn.sl \ mxr \ (Err.sl \ (JType.esl)) \end{aligned}$$

The stack and registers are combined in a coalesced product via *Product.esl* and then embedded into *option* and *err* (as in §3.3) to form the final semilattice for the state space  $\sigma = state-type \ option \ err$ :

$sl :: nat \Rightarrow nat \Rightarrow state\text{-}type\ option\ err\ sl$   
 $sl\ m\!x\!s\ m\!x\!r \equiv Err.sl(Opt.esl(Product.esl(stk\text{-}esl\ m\!x\!s)\ (Err.esl(reg\text{-}sl\ m\!x\!r))))$

The three components of  $sl$  are called *states* (the carrier set), *le* (the ordering), and *sup* (the supremum).

Combining the theorems about the various (err-)semilattice constructions involved in the definition of  $sl$  (starting from Theorem 5), it is easy to prove

**Corollary 7** *If  $\Gamma$  is wellformed then  $sl$  is a semilattice.*

It is trivial to show that  $\top = Err$  is the top element of this semilattice.

### 4.3.2 Type checking

The semilattice construction above gives us an ordering

$\leq' :: state\text{-}type\ option \Rightarrow state\text{-}type\ option \Rightarrow bool$

which is already sufficient to describe type checking.

Type checking is what the original  $\mu$ Java BCV specification does: it describes welltypings by stating conditions for correct method types. In contrast to the type inference in Kildall's Algorithm, we do not need a supremum operation for this.

Since the  $\mu$ Java specification predates our work about executable bytecode verifiers, it looks a bit different from our definition of welltypings  $wt\text{-}app\text{-}eff$  in §3.3. Nevertheless Theorem 10 will connect  $wt\text{-}app\text{-}eff$  to  $wt\text{-}method$  and show that it exactly describes welltypings  $\varphi :: state\text{-}type\ option\ list$  of methods:

$wt\text{-}instr\ i\ \varphi\ pc \equiv$   
 $app\ i\ (\varphi!pc) \wedge$   
 $(\forall pc' \in set\ (succs\ i\ pc). pc' < size\ ins \wedge (eff\ i\ (\varphi!pc) \leq' \varphi!pc'))$

$wt\text{-}start\ \varphi \equiv$   
 $Some\ ([],(OK\ (Class\ C))\#(map\ OK\ ps)\@(replicate\ m\!x\!l\ Err)) \leq' \varphi!0$

$wt\text{-}method\ \varphi \equiv$   
 $0 < size\ ins \wedge wt\text{-}start\ \varphi \wedge (\forall pc. pc < size\ ins \longrightarrow wt\text{-}instr\ (ins!pc)\ \varphi\ pc)$

Again we have some implicit parameters:  $C$  is the class where the method under scrutiny is defined,  $ps$  the types of its formal parameters,  $ins$  the instruction list (method body), and  $m\!x\!l$  the number of local variables. Apart from  $wt\text{-}start$  and the additional condition that the instruction list may not be empty,  $wt\text{-}instr$  and  $wt\text{-}method$  together coincide nicely with  $wt\text{-}app\text{-}eff$

and the fact that *succs* is bounded by *size ins*. Corresponding to the start value of the iteration in Kildall’s algorithm, *wt-start* places a restriction on the welltyping of instruction *0*.

The type checking specification is defined in terms of the functions *app*, *eff* and *succs* as introduced in §3.3. They can directly be reused later for Kildall’s algorithm.

The definition of *succs* is straightforward:

$$\begin{aligned}
succs &:: instr \Rightarrow nat \Rightarrow nat\ list \\
succs\ (Ifempeq\ b)\ pc &= [pc+1, nat\ (pc+b)] \\
succs\ (Goto\ b)\ pc &= [nat\ (pc+b)] \\
succs\ Return\ pc &= [pc] \\
succs\ instr\ pc &= [pc+1]
\end{aligned}$$

Since the offset *b* in branching instructions may be negative, we need to turn the result of *pc+b* back to type *nat* by explicit conversion. The successor of *Return* is again *pc* since lemma 3 requires *succs* to be nonempty for all instructions. The last equation is the default: we just increment the *pc* by one.

Figure 3 and 4 show the definitions of *app* and *eff*. Both are first defined via *app'* and *eff'* on *state-type* and then lifted to *state-type option*. They use the program  $\Gamma$ , the maximum size of the operand stack *mxs*, and the return type *rt* of the current method as implicit parameters.

In *app'* a few new functions occur: *field* is analogous to *method* and looks up declaration information of object fields (defining class and type); apart from that there are only the obvious functions on lists *rev* ::  $\alpha\ list \Rightarrow \alpha\ list$ , *zip* ::  $\alpha\ list \Rightarrow \beta\ list \Rightarrow (\alpha \times \beta)\ list$ , and *take* ::  $nat \Rightarrow \alpha\ list \Rightarrow \alpha\ list$ .

In *eff'* we use *typeof* :: *val*  $\Rightarrow$  *ty option* returning *None* for addresses, and the type of the value otherwise. The *method* expression for *Invoke* merely determines the return type of the method in question. The lifting functional *option-map* ::  $(\alpha \Rightarrow \beta) \Rightarrow \alpha\ option \Rightarrow \beta\ option$  satisfies the two equations *option-map* *f* *None* = *None* and *option-map* *f* (*Some* *x*) = *Some* (*f* *x*).

### 4.3.3 Type inference

If we construct *step* as in §3.3 from *app* and *eff*, we can instantiate Kildall’s algorithm for the  $\mu$ Java VM:

$$\begin{aligned}
kiljvm &:: instr\ list \Rightarrow state\text{-}type\ option\ err\ list \Rightarrow state\text{-}type\ option\ err\ list \\
kiljvm\ ins &\equiv kildall\ le\ sup\ (step\ ins)\ (\lambda pc.\ succs\ (ins!pc)\ pc)
\end{aligned}$$

The proof of the following theorem is easy:

$$\begin{aligned}
& app' :: instr \times state\text{-}type \Rightarrow bool \\
app' (Load\ idx, (st,lt)) &= idx < size\ lt \wedge lt!idx \neq Err \wedge \\
& \quad size\ st < mxs \\
app' (Store\ idx, (t\#\#st,lt)) &= idx < size\ lt \\
app' (LitPush\ v, (st,lt)) &= size\ st < mxs \wedge typeof\ v \neq None \\
app' (Getfield\ F\ C, (t\#\#st,lt)) &= is\text{-}class\ \Gamma\ C \wedge t \preceq Class\ C \wedge \\
& \quad (\exists t'. field\ (\Gamma, C)\ F = Some\ (C, t')) \\
app' (Putfield\ F\ C, (t_1\#\#t_2\#\#st,lt)) &= is\text{-}class\ \Gamma\ C \wedge \\
& \quad (\exists t'. field\ (\Gamma, C)\ F = Some(C, t') \wedge \\
& \quad \quad t_2 \preceq Class\ C \wedge t_1 \preceq t') \\
app' (New\ C, (st,lt)) &= is\text{-}class\ \Gamma\ C \wedge size\ st < mxs \\
app' (Checkcast\ C, (RefT\ t\#\#st,lt)) &= is\text{-}class\ \Gamma\ C \\
app' (Dup, (t\#\#st,lt)) &= 1 + size\ st < mxs \\
app' (IAdd, (t_1\#\#t_2\#\#st,lt)) &= t_1 = PrimT\ Int \wedge t_2 = PrimT\ Int \\
app' (Ifcmpeq\ b, (t\#\#t'\#\#st,lt)) &= (t = t') \vee \\
& \quad (\exists r\ r'. t = RefT\ r \wedge t' = RefT\ r') \\
app' (Goto\ b, s) &= True \\
app' (Return, (t\#\#st,lt)) &= t \preceq rt \\
app' (Invoke\ C\ mn\ ps, (st,lt)) &= size\ ps < size\ st \wedge \\
& \quad method\ (\Gamma, C)\ (mn, ps) \neq None \wedge \\
& \quad let\ as = rev\ (take\ (size\ ps)\ st); \\
& \quad \quad t = st!size\ ps \\
& \quad in\ t \preceq Class\ C \wedge is\text{-}class\ \Gamma\ C \wedge \\
& \quad (\forall (a, f) \in set(zip\ as\ ps). a \preceq f) \\
app' (i, s) &= False \\
\\
app :: instr \Rightarrow state\text{-}type\ option \Rightarrow bool \\
app\ i\ s \equiv case\ s\ of\ None \Rightarrow True \mid Some\ t \Rightarrow app' (i, t)
\end{aligned}$$

Fig. 3. Applicability of instructions

**Lemma 8** *step is monotone and preserves the carrier set.*

With Theorem 1 we therefore get

**Corollary 9** *If  $\Gamma$  is wellformed and succs is bounded by the number of instructions then kiljvm is a bytecode verifier.*

We can further define

$$\begin{aligned}
wt\text{-}kil &\equiv bounded\ (\lambda pc. succs\ (ins!pc)\ pc)\ (size\ ins) \wedge 0 < size\ ins \wedge \\
& \quad let\ S_0 = Some\ ([], (OK\ (Class\ C))\#\#(map\ OK\ ps)\@\@(replicate\ mxl\ Err)); \\
& \quad \varphi_0 = (OK\ S_0)\#\#(replicate\ (size\ ins - 1)\ (OK\ None)); \\
& \quad in\ \forall n < size\ ins. (kiljvm\ \varphi_0)!n \neq Err
\end{aligned}$$

This definition is again in the context of a method, i.e. it uses the implicit parameters  $ins$  (instruction sequence),  $C$  (defining class),  $ps$  (formal parameter

$$\begin{aligned}
\text{eff}' &:: \text{instr} \times \text{state-type} \Rightarrow \text{state-type} \\
\text{eff}' (\text{Load } idx, (st, lt)) &= (\text{ok-val } (lt!idx) \# st, lt) \\
\text{eff}' (\text{Store } idx, (t \# st, lt)) &= (st, lt[idx := OK t]) \\
\text{eff}' (\text{LitPush } v, (st, lt)) &= (\text{the } (\text{typeof } v) \# st, lt) \\
\text{eff}' (\text{Getfield } F C, (t \# st, lt)) &= (\text{snd } (\text{the } (\text{field } (\Gamma, C) F)) \# st, lt) \\
\text{eff}' (\text{Putfield } F C, (t_1 \# t_2 \# st, lt)) &= (st, lt) \\
\text{eff}' (\text{New } C, (st, lt)) &= (\text{Class } C \# st, lt) \\
\text{eff}' (\text{Checkcast } C, (t \# st, lt)) &= (\text{Class } C \# st, lt) \\
\text{eff}' (\text{Dup}, (t \# st, lt)) &= (t \# t \# st, lt) \\
\text{eff}' (\text{IAdd}, (t_1 \# t_2 \# st, lt)) &= (\text{PrimT Int} \# st, lt) \\
\text{eff}' (\text{Ifcmpeq } b, (t_1 \# t_2 \# st, lt)) &= (st, lt) \\
\text{eff}' (\text{Goto } b, s) &= s \\
\text{eff}' (\text{Return}, s) &= s \\
\text{eff}' (\text{Invoke } C mn ps, (st, lt)) &= \text{let } st' = \text{drop } (1 + \text{size } ps) \text{ } st; \\
&\quad (-, rt, -, \rightarrow, -) = \text{the } (\text{method } (\Gamma, C) (mn, ps)) \\
&\quad \text{in } (rt \# st', lt) \\
\\
\text{eff} &:: \text{instr} \Rightarrow \text{state-type option} \Rightarrow \text{state-type option} \\
\text{eff } i &\equiv \text{option-map } (\lambda s. \text{eff}' (i, s))
\end{aligned}$$

Fig. 4. Effect of instructions on the state type

types), and  $maxl$  (number of local variables). From corollary 9 together with lemmas 3 and 4 we obtain the following theorem.

**Theorem 10** *If the program  $\Gamma$  is wellformed, if  $\varphi_0$  is defined as in  $wt\text{-kil}$ , and if  $wt\text{-kil}$  holds for a method, then  $wt\text{-method}$  ( $kiljvm \varphi_0$ ) holds for the same method.*

Although useful on its own in the form above, theorem 10 becomes clearer rephrased: if  $\Gamma$  is wellformed and  $wt\text{-kil}$  holds for a method, then this method is type correct (there exists a welltyping  $\varphi$  such that  $wt\text{-method } \varphi$ ). It connects the executable BCV with the static description of welltypings.

#### 4.3.4 Type safety

The most interesting property we have proved about  $wt\text{-method}$  is type safety. So far we have considered an abstraction of the JVM that works on types rather than values. It remains to show that this abstraction is faithful, i.e. that welltypedness on this abstract level actually guarantees the absence of type errors in the concrete  $\mu\text{JVM}$  operating on values.

The original  $\mu\text{Java}$  BCV specification is by Pusch [3] who showed that it implies type soundness of the concrete  $\mu\text{JVM}$ . Roughly speaking, she proved that during execution of welltyped code, all values conform to the types given in the welltyping. That is, she defined a conformance relation  $\text{correct-state } s \Phi$

between a concrete machine state  $s$  (of HOL type *jvm-state*) and a program type  $\Phi :: \text{cname} \Rightarrow \text{sig} \Rightarrow \text{state-type option list}$  ( $\varphi$  lifted to programs, see also §A.1 for the definition conformance). And she proved

$$\text{wt-jvm-prog } \Phi \wedge \text{correct-state } s \Phi \wedge s \xrightarrow{\text{jvm}} t \longrightarrow \text{correct-state } t \Phi$$

where *wt-jvm-prog* is again *wt-method* lifted to programs, i.e. *wt-jvm-prog* holds if *wt-method* holds for all methods in all classes of  $\Gamma$ . The theorem says that a welltyping guarantees type safe execution.

We can also characterize concrete starting states  $s$  for which execution is type safe without referring to the calculated welltyping at all: if *wt-kil* holds then it suffices to look at the start value  $\varphi_0$  defined in *wt-kil*. States  $s$  satisfying *correct-state*  $s \Phi_0$  (where  $\Phi_0$  is again  $\varphi_0$  lifted to programs) will also satisfy *correct-state*  $s \Phi$  since the result  $\Phi$  of Kildall’s algorithm lies above its start value and *correct-state* is monotone.

## 5 Object Initialization

With *object initialization* we address a particular feature of the Java bytecode verifier: the test whether each new object is properly initialized before it is used. This not only includes a guarantee that for each object its constructor is called before its fields or methods are accessed, but also that each constructor calls the constructor of the object’s superclass before it returns or begins with the rest of the initialization process.

Why care at all about object initialization? The question is not as rhetoric as it may seem—object initialization is not necessary for the type safety of the language, the default values of fields do nicely. After all, we have just claimed in section 4 that  $\mu\text{Java}$  is type safe and there was no mention of object initialization anywhere. The feature is interesting because large parts of Java’s security mechanisms depend on constructors being called before objects are used and also on superclass constructors being called before the own constructor is executed. In this way and together with access modifiers secure, consistent object states can be guaranteed.

The purpose of this section is twofold. On the one hand it presents the formalization of an additional important and nontrivial feature of the Java BCV together with its proof of correctness. On the other hand it demonstrates the flexibility of our BCV framework by showing how it deals with significant changes and extensions to the specification of the existing  $\mu\text{Java}$  BCV. Not only do we now want to prove stronger properties of the bytecode verifier, but also the type system is now a new one, and the instruction set along with its

operational semantics is slightly different.

We extend our model in four steps: First we explain how object initialization works and define a type system for it in §5.1. Then we proceed in §5.2 with the changes to the VM and the operational semantics. In §5.3 we define the BCV for object initialization. Finally, type safety relates the latter two in §5.4.

### 5.1 The new type system

The JVM specification [13, pages 131–133] gives a short description how to check for proper object initialization. Figure 5 is a typical piece of bytecode for the object creation/constructor call cycle as it is produced by common Java compilers (the example from [13] translated to  $\mu$ Java).

...	
<i>New A</i>	Allocate new space for class A
<i>Dup</i>	Duplicate reference on the stack
<i>Load 0</i>	Push constructor parameter
<i>Invoke-special A (init, [Class B])</i>	Invoke constructor on class A
...	

Fig. 5. A typical object creation/constructor call code piece

The instruction sequence first allocates space for the still uninitialized object, and then duplicates the reference to that object for the constructor call. After the constructor call the reference to the newly created and initialized object is on top of the operand stack.

To deal with that kind of situations, the JVM specification proposes to introduce two new artificial types that mark not yet initialized values; we will call them *UnInit* and *PartInit*.

As the name suggests, *UnInit* stands for uninitialized, freshly created objects. The reference on top of the stack after the *New A* instruction would get the type *UnInit A*. After the constructor has been called on that reference the object is initialized and we can replace *UnInit A* by our usual type *Class A*. As the JVM specification [13, pages 132–133] points out, that is not enough, though:

The instruction number needs to be stored as part of the special type, as there may be multiple not-yet-initialized instances of a class in existence on the operand stack at one time. For example, the Java virtual machine instruction sequence that implements

```
new InputStream(new Foo(), new InputStream("foo"))
```

may have two uninitialized instances of `InputStream` on the operand stack at

once. When an instance initialization method is invoked on a class instance, only those occurrences of the special type on the operand stack or in the local variable array that are the same object as the class instance are replaced.

By storing the program counter of the instruction that created a reference, we in fact implement a simple alias analysis: the type entry  $UnInit\ C\ pc$  keeps track of one specific value (the reference created by the instruction  $New\ C$  at position  $pc$ ).

The  $PartInit$  type is easier: it marks the type of local variable  $l$  (the *this* pointer) in constructors. It can be replaced by the normal type of the class as soon as the superclass constructor has been invoked.

The following datatype definition captures the above formally; the constructor  $Init\ ty$  stands for the normal, initialized Java types.

$$datatype\ init\text{-}ty = Init\ ty \mid UnInit\ cname\ nat \mid PartInit\ cname$$

Our new type system in the BCV has  $init\text{-}ty$  wherever there was  $ty$  before, i.e. on the stack, or beneath the  $OK/Err$  structure in the local variables.

The BCV is also required to check that the superclass constructor has been called on all paths out of the constructor. For this we extend the state type by a component of HOL type  $bool$ . It is set to  $True$  when the superclass constructor is called, and checked for in the  $Return$  instruction rule when we are verifying a constructor. Since most of our typing rules remain the same as without the additional  $bool$  level, we introduce a new  $state\text{-}bool$ , and  $state\text{-}type$  is now:

$$\begin{aligned} state\text{-}type &= init\text{-}ty\ list \times init\text{-}ty\ err\ list \\ state\text{-}bool &= state\text{-}type \times bool \end{aligned}$$

To deal with unreachable code, the BCV is again lifted to  $state\text{-}bool\ option$ .

Figure 6 shows a welltyping for the instructions of figure 5 with the new state type. To fit it on the page we have left out the  $option$  and  $bool$  component.

$pc$	instruction	$state\text{-}type$
4	...	
5	$New\ A$	( [], [Init (Class B)])
6	$Dup$	( [UnInit A 5], [Init (Class B)])
7	$Load\ 0$	( [UnInit A 5, UnInit A 5], [Init (Class B)])
8	$Inv\text{-}spl\ A\ (init, [Class\ B])$	( [Init (Class B), UnInit A 5, UnInit A 5], [Init (Class B)])
9	...	( [Init (Class A)], [Init (Class B)])

Fig. 6. Example of a welltyping with object initialization

## 5.2 Operational semantics

This section covers the changes to the operational semantics we need to model constructor calls and their correct handling in the BCV.

For one we obviously need constructors. In the real JVM, constructors are methods with a special name `<init>` and no return value. Similarly, in  $\mu$ Java constructors are ordinary methods with a special name `init`, but they may have a return value as any other method (which we have ignored in figure 6).

We also need a bytecode instruction to call constructors: *Invoke-spl*. Similar to the *Invoke* instruction, it has a class and a method signature as parameters. In contrast to *Invoke* it does not do a virtual method call, but a static one.  $\mu$ Java does not contain static methods apart from constructors; therefore *Invoke-spl* can only be used for constructor calls.

Since we want to prove something about how far objects are initialized, we need to observe the initialization status of individual objects. The current  $\mu$ Java VM state does not allow this: from heap, stack and local variables we do not get any information how far an object is initialized. Thus we introduce a new, artificial component into the state: a second heap, in structure mirroring the real one, that stores the initialization status using values of HOL type *init-ty*. Formally, the new component *iheap* is simply a function from locations to *init-ty*. At this point the class name parameter of *PartInit* comes into play: if an object gets the tag *PartInit C* it means that the object is initialized up to class *C* in the class hierarchy, or, more precisely, that the constructor of *C*'s superclass must be called before the fields of the object can be accessed.

This new component of the  $\mu$ JVM state only plays the role of an auxiliary variable for the type safety proof and does not alter the observable behaviour of the former  $\mu$ Java VM.

Unfortunately recording the initialization status of objects induces another problem: in our type safety proof we rely on a large invariant which requires that objects once allocated do not change their type on the heap. Values may change of course, and new objects may be created, but the type information of existing objects (that the invariant relates to what the BCV has predicted) may not change. This also applies to the new *iheap* component. Observing the changes during the lifetime of objects is the very purpose of *iheap*, so we need to accommodate it somehow. We use the solution Freund proposes in [23]: the invariant may not allow type changes, but it does allow to create new objects. So for each constructor call we create a new blank object which is a copy of the object before, only the initialization status tag gets updated. When a constructor is finished, it replaces the uninitialized reference in the calling method by the now initialized own object.

This sounds like a large modification, but on closer inspection there is again no observational difference in executions between creating new objects (and then discarding the superfluous ones) and the standard semantics.

The initialization process is basically a chain of constructor calls along the class hierarchy. It is finished when the constructor of class *Object* has been called. Before that, in between constructor calls, the object is inaccessible. During the whole process all fields retain their default value, and no method other than the constructor can be invoked on it. Once the end of the chain is reached with the constructor of class *Object*, we just work on the last allocated object. Because each constructor replaces the object of the calling constructor, all intermediate objects are discarded.

In order to replace the correct reference when a constructor is finished, we extend our definition of call frames from §4.1 by a pair of references. In that pair we store the reference the current constructor has been called to initialize and the reference to the fully initialized object. Since in the beginning there will be no fully initialized object, we will store *Null* in that case. To avoid annoying type conversions, we work with values *val* instead of references (although we will only be using addresses and *Null*). So *frame* is now:

$$frame = opstack \times locvars \times cname \times sig \times nat \times (val \times val)$$

The definition of *frame* applies to all methods, but only constructors will use the additional component, all other methods will simply ignore it. Adding the *iheap* extension we finally get the new state space of the  $\mu$ Java VM:

$$\begin{aligned} jvm\text{-}state &= xcpt\ option \times aheap \times iheap \times frame\ list \\ iheap &= loc \Rightarrow init\text{-}ty \end{aligned}$$

With this in mind we can take a look at how the formal definition of the operational semantics changes. The only interesting instructions are *New*, *Invoke-spl*, and *Return*. The rules for the other instructions get two new parameters, *ihp* for initialization status and *z* for the reference update in constructors, but they simply pass them on. The rule for *Load* for instance looks like this:

$$\begin{aligned} exec\text{-}instr\ (Load\ idx)\ hp\ ihp\ stk\ vars\ Cl\ sig\ pc\ z\ frs = \\ (None,\ hp,\ ihp,\ ((vars\ !\ idx)\ \#\ stk,\ vars,\ Cl,\ sig,\ pc+1,\ z)\ \#frs) \end{aligned}$$

For the *New* instruction we have to record that freshly created objects are completely uninitialized. Apart from that everything remains the same (see also §4.2, p. 17):

```

exec-instr (New C) hp ihp stk vars Cl sig pc z frs =
let (ref, xp') = new-Addr hp;
    hp' = if xp'=None then hp(ref := Some (blank C)) else hp;
    ihp' = if xp'=None then ihp(ref := UnInit C pc) else ihp';
    stk' = if xp'=None then (Addr ref)#stk else stk
in (xp', hp', ihp', (stk', vars, Cl, sig, pc+1, z)#frs)

```

The definition of *Invoke-spcl* is new:

```

exec-instr (Invoke-spcl C mn ps) hp ihp stk vars Cl sig pc z frs =
let    n = size ps;
    args = take n stk;
    ref = stk!n;
    x1 = raise-xcpt (ref=None) NullPointer;
    D = fst(the(hp the-Addr ref));
    (dc, -, -, mxl, _) = the (method (Γ, C) (mn, ps));
    (a', x2) = new-Addr hp;
    xp' = if x1 = None then x2 else x1;
    hp' = hp(a' := Some (blank D));
    T = if C = Object then Init (Class D) else PartInit C;
    z' = if C = Object then (Addr a', Addr a') else (Addr a', Null);
    frs' = if xp'≠None then [] else
        [([], (Addr a')#(rev args)@(replicate mxl arbitrary), dc, (mn, ps), 0, z')]
in (xp', hp', ihp(a' := T), frs'@(stk, vars, Cl, sig, pc, z)#frs)

```

The beginning is the same as *Invoke*: in *args* we store the actual parameters of the constructor call. The reference on which to invoke the constructor is the next element on the stack after the parameters. If it is *Null*, a *NullPointer* exception is thrown. Still as in *Invoke* we retrieve the dynamic type *D* of the object the reference *ref* points to, but this time we do not use it for a dynamic method lookup; we do a static method lookup instead, with the parameters *C* (the class), *mn* (the method name), and *ps* (the list of parameter types) of the *Invoke-spcl* instruction. Then we create a new object: as in the *New* instruction, we request a free location, handle the *OutOfMemory* exception should one occur, and assign a blank object to the new address with the same dynamic type as the one at *ref* (in effect, we copy the object at *ref* to the new address *Addr a'*).

Now we come to the init status *ihp'*: the new object gets type *PartInit C* since the constructor for class *C* has just been invoked, and the next constructor to be invoked must be one in the superclass of *C*. If *C* has no superclass, i.e. if *C = Object* then we have reached the end of the constructor chain and can view the new object as fully initialized. After this its fields and methods are accessible, first from the constructor body of class *Object*, then in turn from each constructor in the call chain. At this point we also have to store in the frame of the new constructor the pair *z'* that tells which reference the

constructor is supposed to initialize and with which reference to replace *ref* when it is finished. The former is the new object at *Addr a'*. The latter we do not know yet, so we set it to *Null*. Only if we have reached *Object*, we know that our newly created object is also the final one that will replace all intermediate objects in the call chain. In this setting we construct the call frame of the constructor to be invoked just as the normal *Invoke* does.

The *Return* instruction now additionally handles the reference update at constructor returns:

```

exec-instr Return hp ihp stk0 vars Cl sig0 pc z0 frs =
if frs=[] then (None, hp, ihp, []) else
let (stk,loc,C,sig,pc,z) = hd frs;
    val = hd stk0;
    (mn,ps) = sig0;
    (a,b) = z0;
    n = size ps;
    args = take n stk;
    addr = stk!n;
    drpstk = drop (n+1) stk;
    stk' = if mn=init then val#(replace addr b drpstk) else val#drpstk;
    loc' = if mn=init then replace addr b loc else loc;
    z' = if mn=init ∧ z = (addr,Null) then (addr,b) else z
in (None, hp, ihp, (stk',loc',C,sig,pc+1,z')#tl frs)

```

We will first take another look at the parameters: *stk<sub>0</sub>*, *sig<sub>0</sub>* and *z<sub>0</sub>* are the stack, the signature and the reference update pair of the current call frame, i.e. the own method. As in §4.2, we extract the return value *val*, our own method name *mn* and our own list of formal parameter types *ps*. Then we drop the actual parameters from the stack in the caller frame. If we are not in a constructor, i.e. if our own method name *mn* is not *init*, we just put the return value on top of the stack in the caller frame and are done. If we are returning from a constructor, however, we use *replace* to substitute the now initialized object *b* (the second component of our reference update pair) for the address of the original object *addr*. This replacement must occur everywhere on the stack and in the local variables of the caller frame to delete all references to the original object. If the caller frame belongs to a constructor that is initializing the same object as we are, we also have to modify the second component of its reference update pair *z* to the initialized object *b*.

### 5.3 Bytecode verification

Since we already defined types for object initialization in §5.1 we now only have to construct a semilattice for them and change the transfer function

accordingly to instantiate the BCV.

The ordering of the semilattice is canonical: *PartInit* and *UnInit* are only related to themselves, for *Init t* we use the old  $\preceq$ . Formally:

$$\begin{array}{rcl} \text{Init } t_1 & \preceq_i & \text{Init } t_2 \\ a & \preceq_i & b \end{array} = \begin{array}{l} t_1 \preceq t_2 \\ (a = b) \end{array}$$

The carrier set is constructed easily and the supremum operation again follows the ordering canonically:

$$\text{init-tys} \equiv \{ \text{Init } x \mid x \in (\text{types } G) \} \cup \{ x \mid \exists C n. x = \text{UnInit } C n \} \cup \{ x \mid \exists C. x = \text{PartInit } C \}$$

$$\begin{aligned} \text{sup } (\text{Init } t_1) (\text{Init } t_2) &= \text{case } J\text{Type.sup } t_1 t_2 \text{ of} \\ &\quad \text{Err} \Rightarrow \text{Err} \mid \text{OK } x \Rightarrow \text{OK } (\text{Init } x) \\ \text{sup } a b &= \text{if } a = b \text{ then OK } a \text{ else Err} \end{aligned}$$

With this we define *Init.esl*  $\equiv$  (*init-tys*,  $\preceq_i$ , *sup*) as the err-semilattice for *init-ty* and arrive at:

**Lemma 11** *If  $\Gamma$  is wellformed then *Init.esl* is an err-semilattice.*

If we repeat the construction in §4.3, we get:

$$\begin{aligned} \text{stk-esl} &:: \text{nat} \Rightarrow \text{init-ty list esl} \\ \text{stk-esl } m\text{xs} &\equiv \text{upto-esl } m\text{xs } \text{Init.esl} \end{aligned}$$

$$\begin{aligned} \text{reg-sl} &:: \text{nat} \Rightarrow \text{init-ty err list sl} \\ \text{reg-sl } m\text{xr} &\equiv \text{Listn.sl } m\text{xr } (\text{Err.sl } \text{Init.esl}) \end{aligned}$$

$$\begin{aligned} \text{sl} &:: \text{nat} \Rightarrow \text{nat} \Rightarrow \text{state-bool option err sl} \\ \text{sl } m\text{xs } m\text{xr} &\equiv \text{Err.sl}(\text{Opt.esl}(\text{Product.esl}(\text{Product.esl} \\ &\quad (\text{stk-esl } m\text{xs}) (\text{Err.esl}(\text{reg-sl } m\text{xr}))) (\text{TrivLat.esl}::\text{bool } \text{esl}))) \end{aligned}$$

where *TrivLat.esl::bool* is the trivial err-semilattice (with = as ordering) applied to type *bool*.

**Lemma 12** *If  $\Gamma$  is wellformed then *sl* is also a semilattice.*

The second ingredient to the BCV is the transfer function. In figures 7 and 8 we define *eff'* and *app'* for *init-ty*. They both work on *state-type* (where *ty* is replaced by *init-ty*), and do not involve the *bool* and *option* component yet.

Compared to the original version in figures 4 (p. 24) and 3 (p. 23), both definitions have become a bit larger, but remained the same in structure. With *pc*, the program counter of the current instruction, they receive one

$$\begin{aligned}
\text{eff}' &:: \text{instr} \times \text{state-type} \Rightarrow \text{state-type} \\
\text{eff}' (\text{Load } idx, (st, lt)) &= (\text{ok-val } (lt!idx)\#st, lt) \\
\text{eff}' (\text{Store } idx, (t\#st, lt)) &= (st, lt[idx:= OK t]) \\
\text{eff}' (\text{LitPush } v, (st, lt)) &= (\text{Init } (\text{the } (\text{typeof } v))\#st, lt) \\
\text{eff}' (\text{Getfield } F C, (t\#st, lt)) &= (\text{Init } (\text{snd } (\text{the } (\text{field } (\Gamma, C) F)))\#st, lt) \\
\text{eff}' (\text{Putfield } F C, (t_1\#t_2\#st, lt)) &= (st, lt) \\
\text{eff}' (\text{New } C, (st, lt)) &= (\text{UnInit } C pc\#st, \\
&\quad \text{replace } (\text{OK } (\text{UnInit } C pc)) \text{ Err } lt) \\
\text{eff}' (\text{Checkcast } C, (t\#st, lt)) &= (\text{Init } (\text{Class } C) \# st, lt) \\
\text{eff}' (\text{Dup}, (t\#st, lt)) &= (t\#t\#st, lt) \\
\text{eff}' (\text{IAdd}, t_1\#t_2\#st, lt) &= (\text{Init } (\text{PrimT Integer})\#st, lt) \\
\text{eff}' (\text{Ifcmpeq } b, (t_1\#t_2\#st, lt)) &= (st, lt) \\
\text{eff}' (\text{Goto } b, s) &= s \\
\text{eff}' (\text{Return}, s) &= s \\
\text{eff}' (\text{Invoke } C mn ps, (st, lt)) &= \text{let } st' = \text{drop } (1+\text{size } ps) st; \\
&\quad (-, rt, -, -, -) = \text{the } (\text{method } (\Gamma, C) (mn, ps)) \\
&\quad \text{in } (\text{Init } rt\#st', lt) \\
\text{eff}' (\text{Invoke-spl } C mn ps, (st, lt)) &= \text{let } t = st!\text{size } ps; \\
&\quad i = \text{Init } (\text{theClass } t); \\
&\quad st'' = \text{drop } (1+\text{size } ps) st; \\
&\quad st' = \text{replace } t i st''; \\
&\quad lt' = \text{replace } (\text{OK } t) (\text{OK } i) lt; \\
&\quad (-, rt, -, -, -) = \text{the } (\text{method } (\Gamma, C) (mn, ps)) \\
&\quad \text{in } (\text{Init } rt\#st', lt')
\end{aligned}$$

Fig. 7. Effect of instructions on the state type with object initialization

more implicit parameter.

In  $\text{eff}'$  the instructions *Load*, *Store*, *Putfield*, *Ifcmpeq*, *Goto*, *Return*, and *Dup* remain unchanged; the instructions *LitPush*, *Getfield*, *CheckCast*, *IAdd*, and *Invoke* now explicitly yield initialized values. The instructions *Invoke-spl* and *New* are more interesting.

*Invoke-spl* is similar to *Invoke*, but it can only be used on uninitialized references (which is checked in *app*). After the constructor returns, the reference will be fully initialized, so the *Invoke-spl* rule replaces the uninitialized type  $t$  with an initialized one of the same class ( $\text{theClass} :: \text{ini-ty} \Rightarrow \text{ty}$  satisfies  $\text{theClass } (\text{PartInit } C) = \text{Class } C$  and  $\text{theClass } (\text{UnInit } C pc) = \text{Class } C$ ). The replacement again happens everywhere in the stack and local variables.

The *New* instruction seems easy at first sight: if a *New*  $C$  is at position  $pc$ , it produces the type  $\text{UnInit } C pc$ . The rule in figure 7 does a bit more, though. For the alias analysis to be correct, there must not be any former instances of the type  $\text{UnInit } C pc$  with the same  $pc$  on the stack or in the local variables. We take care of this with the *replace* for local variables in  $\text{eff}'$  and with

$$\begin{aligned}
& app' :: instr \times state\text{-}type \Rightarrow bool \\
app' (Load\ idx, (st,lt)) &= idx < lt \wedge lt!.idx \neq Err \wedge \\
& \quad size\ st < mxs \\
app' (Store\ idx, (t\#st,lt)) &= idx < size\ lt \\
app' (LitPush\ v, (st,lt)) &= size\ st < mxs \wedge typeof\ v \neq None \\
app' (Getfield\ F\ C, (t\#st,lt)) &= is\text{-}class\ \Gamma\ C \wedge t \preceq_i Init\ (Class\ C) \wedge \\
& \quad (\exists t'. field\ (\Gamma, C)\ F = Some\ (C, t')) \\
app' (Putfield\ F\ C, (t_1\#t_2\#st,lt)) &= is\text{-}class\ \Gamma\ C \wedge \\
& \quad (\exists t'. field\ (\Gamma, C)\ F = Some\ (C, t') \wedge \\
& \quad \quad t_2 \preceq_i Init\ (Class\ C) \wedge t_1 \preceq_i Init\ t') \\
app' (New\ C, (st,lt)) &= is\text{-}class\ \Gamma\ C \wedge size\ st < mxs \wedge \\
& \quad UnInit\ C\ pc \notin set\ st \\
app' (Checkcast\ C, t\#st,lt)) &= is\text{-}class\ \Gamma\ C \wedge (\exists r. t = Init\ (RefT\ r)) \\
app' (Dup, (t\#st,lt)) &= 1 + size\ st < mxs \\
app' (IAdd, (t_1\#t_2\#st,lt)) &= t_1 = t_2 \wedge t_1 = Init\ (PrimT\ Integer) \\
app' (Ifcmpeq\ b, (t_1\#t_2\#st,lt)) &= t_1 = t_2 \vee (\exists r\ r'. t_1 = Init\ (RefT\ r) \wedge \\
& \quad t_2 = Init\ (RefT\ r')) \\
app' (Goto\ b, s) &= True \\
app' (Return, (t\#st,lt)) &= t \preceq_i Init\ rt \\
app' (Invoke\ C\ mn\ ps, (st,lt)) &= size\ ps < size\ st \wedge mn \neq init \wedge \\
& \quad method\ (\Gamma, C)\ (mn, ps) \neq None \wedge \\
& \quad let\ as = rev\ (take\ (size\ ps)\ st); \\
& \quad \quad t = st!.size\ ps \\
& \quad in\ t \preceq_i Init\ (Class\ C) \wedge is\text{-}class\ \Gamma\ C \wedge \\
& \quad \quad (\forall (a, f) \in set\ (zip\ as\ ps). a \preceq_i Init\ f) \\
app' (Invoke\ spcl\ C\ mn\ ps, (st,lt)) &= size\ ps < size\ st \wedge mn = init \wedge \\
& \quad (\exists r. method\ (\Gamma, C)\ (mn, ps) = Some\ (C, r)) \wedge \\
& \quad let\ as = rev\ (take\ (size\ ps)\ st); \\
& \quad \quad t = st!.size\ ps \\
& \quad in\ is\text{-}class\ \Gamma\ C \wedge \\
& \quad \quad ((\exists pc. t = UnInit\ C\ pc) \vee \\
& \quad \quad t = PartInit\ C' \wedge (C', C) \in subcl\ \Gamma) \wedge \\
& \quad \quad (\forall (a, f) \in set\ (zip\ as\ ps). a \preceq_i (Init\ f))
\end{aligned}$$

Fig. 8. Applicability of instructions with object initialization

checking for  $UnInit\ C\ pc$  on the stack in  $app'$ . Sun's JVM specification solves the problem by disallowing all backwards jumps as long as there is any  $UnInit$  type anywhere on the stack or in the local variables. This is correct, but somewhat drastic: although the restriction is not severe for programming in Java, it rejects an unnecessary large number of type safe programs. At the same time it is hard to reason about, because it makes assumptions about the dataflow analysis itself and not only about properties of the resulting welltyping. Freund leaves the local variables untouched, but checks for  $UnInit$  in  $app$  (if we translate his notation into our framework). This has the drawback

of not being monotone (more specifically the local variable part of  $app$  would not be monotone).

We use the solution of [10]; it yields a monotone transfer function, admits as many programs and is still safe. In fact, here  $replace$  is the identity function, because the first path to  $New$  in the dataflow analysis cannot contain  $UnInit\ C\ pc$  (because only the instruction at  $pc$  creates the type  $UnInit\ C\ pc$ ). A merge with subsequent paths that might contain  $UnInit\ C\ pc$  would already give  $Err$  without  $replace$ . This fact is difficult to prove formally, because it again involves reasoning about the dataflow analysis ( $paths$ ); just looking at the welltyping is not enough.

Since  $eff'$  does not yet include the new  $bool$  component of  $state\text{-}bool$ , we need one more lifting step. The purpose of the  $bool$  part in the state type is to mark whether a constructor has already called its superclass constructor. We set it to  $True$  when we call  $Invoke\text{-}spl$  for a partly initialized type ( $app$  checks that it is the superclass constructor), otherwise we leave it untouched:

$$\begin{aligned} eff\text{-}bool &:: instr \Rightarrow state\text{-}bool \Rightarrow state\text{-}bool \\ eff\text{-}bool\ i\ ((st,lt),z) &\equiv \\ (eff'\ (i,(st,lt)), & \\ if\ \exists\ C\ m\ p\ D.\ i = Invoke\text{-}spl\ C\ m\ p \wedge st.size\ p = PartInit\ D & \text{ then } True\ \text{ else } z) \end{aligned}$$

$$\begin{aligned} eff &:: instr \Rightarrow state\text{-}bool\ option \Rightarrow state\text{-}bool\ option \\ eff\ i\ pc &\equiv option\text{-}map\ (eff\text{-}bool\ i\ pc) \end{aligned}$$

The definition of  $app'$  in figure 8 looks intimidating, but compared to figure 3 the changes are again small:  $Load$ ,  $Store$ ,  $LitPush$ ,  $Dup$  and  $Goto$  are the same.  $Getfield$ ,  $Putfield$ ,  $CheckCast$ ,  $IAdd$ ,  $Ifcmpeq$ , and  $Return$  only restrict their arguments to initialized types.  $New$  we have already mentioned in the discussion of its  $eff'$  rule. Normal method invocation is also restricted to initialized objects (for the parameters as well as for the object on which to invoke the method). Additionally, the method must not be a constructor. Finally, there is the new rule for  $Invoke\text{-}spl$ . It is similar to  $Invoke$ , but here we make sure that we actually call a constructor ( $mn = init$ ). Since it is supposed to be a static method invocation, the method dictionary  $method$  must yield an entry telling us that the method is defined in class  $C$  (and not in a superclass of it). The rule ensures that the type  $t$  of the object on which to invoke the constructor is either completely uninitialized, or partly initialized. If it is uninitialized it must be of type  $UnInit\ C\ pc$  (for some  $pc$ )—only then are we allowed to invoke the  $C$  constructor. If it is partly initialized it must be of type  $PartInit\ C'$  (where the implicit parameter  $C'$  is the class we are currently verifying). This is because if it is partly initialized, then we are verifying a constructor (only there may partly initialized objects occur). It must be initialized up to exactly  $C'$ , because for any class  $D$  the type  $PartInit\ D$  may only occur in  $D$  constructors. The only thing a constructor may do with

partly initialized objects is invoke the superclass constructor on them—so  $C$  must be the direct superclass of  $C'$ . The JVM specification also allows to call another constructor of the own class, not only one of the superclass. In practice this is convenient, in our formalization it would just add one more uninteresting case (where  $C=C'$ ) to all proofs about *Invoke-spcl*.

Again, we have to lift  $app'$  to the new *bool* component and then the *option* type. This time we do it in one step: for *None* we again get *True*, for *Some* first  $app'$  must be satisfied, then we have two additional conditions on the superclass-constructor-has-been-called marker  $z$ :

$$\begin{aligned}
&app :: instr \Rightarrow state\text{-}bool\ option \Rightarrow bool \\
&app\ i\ s \equiv case\ s\ of\ None \Rightarrow True \mid Some\ t \Rightarrow \\
&let\ ((st,lt),z) = t\ in \\
&\quad app'\ (i,(st,lt)) \wedge \\
&\quad mn = init \longrightarrow \\
&\quad ((i = Return \longrightarrow z) \wedge \\
&\quad (\forall C\ m\ p.\ i = Invoke\text{-}splc\ C\ m\ p \wedge st!size\ p = PartInit\ C' \longrightarrow \neg z))
\end{aligned}$$

If we are verifying a constructor then at each *Return* instruction the marker must be *True*, and at each *Invoke-spcl* for partly initialized objects the marker must be *False* (we may call the superclass constructor only once;  $C'$  is again the current class).

With case distinction over the instruction set, we get:

**Lemma 13** *eff and app are monotone.*

The instantiation of Kildall's algorithm remains the same, it just works with other abstract execution functions *eff* and *app* and a slightly different type system. The only thing we need to adjust is the start value of the iteration.

$$\begin{aligned}
&wt\text{-}kil \equiv \\
&bounded\ (\lambda n.\ succs\ (ins!n)\ n)\ (size\ ins) \wedge 0 < size\ ins \wedge \\
&let\ t = OK\ (if\ mn = init \wedge C \neq Object\ then\ PartInit\ C\ else\ Init\ (Class\ C)); \\
&\quad S_0 = Some\ (([],t\#(map\ (OK \circ Init)\ ps))\@\ (replicate\ mxl\ Err)),C=Object); \\
&\quad \varphi_0 = (OK\ S_0)\#\ (replicate\ (size\ ins-1)\ (OK\ None)) \\
&in\ \forall n < size\ ins.\ (kiljvm\ \varphi_0)!n \neq Err
\end{aligned}$$

The *this* pointer  $t$  (local variable  $\theta$ ) is a bit more complicated than before: if we verify a constructor, and if it is not the constructor of class *Object* then the *this* pointer is only partly initialized yet—the superclass constructor has to be called before it can be used. Otherwise, if it is a normal method or if it is class *Object*, we may assume that the *this* pointer is an initialized object. The only new thing in the rest of the start value is that we may also assume that the parameters only contain initialized values.

Since our type system is a semilattice, the ordering meets the ascending chain condition when  $\Gamma$  is wellformed, *step* built of *eff* and *app* is monotone and preserves the carrier set, we get from Theorem 1:

**Lemma 14** *If  $\Gamma$  is wellformed and *succs* is bounded by the number of instructions then *kilvm* is a bytecode verifier.*

When we also replace *OK* (Class *C*) in *wt-start* by *t* as in *wt-kil*, we obtain the desired connection between BCV and welltypings:

**Theorem 15** *If a program  $\Gamma$  is wellformed and *wt-kil* holds for a method, then the method is type correct w.r.t. *wt-method*.*

Let us revisit what we needed to do in this section to instantiate the framework for the new type system. The largest piece of work were the new definitions of *eff* and *app*. They are necessary anyway, they not only define the executable BCV the framework provides, but they also describe the welltypings we will need in the type safety proof. The additional work was the semilattice construction, the proof that *eff* and *app* are monotone, and the proof that *eff* and *app* preserve the carrier set. The latter two proofs are case distinctions on the instruction set and are handled very well by Isabelle’s automatic tactics (with a few relatively easy lemmas about the new types). The semilattice constructions were straightforward since *Init.esl* is easy and for the rest we could use the theories of §2.

We may summarize that while it is considerable work to extend a large formalization by a new feature, the BCV framework presented here handles these changes very well. Due to its modularity and abstractness the amount of additional work needed for the framework is very small in comparison to a monolithic development.

#### 5.4 Type Safety

In this section we investigate if the BCV with object initialization really does what we expect it to do. The type safety proof is still an invariant proof as in §4.3.4 and the main theorem is still that in welltyped programs execution preserves the invariant. The proof itself, a case distinction over the instruction set, is large, but it is not the hard part. The challenge is finding the right invariant.

The invariant should ensure that runtime types are approximated correctly by the static welltyping. With object initialization we also want the new *iheap* component of the  $\mu$ Java VM state to agree with the initialization status the BCV predicts. This is the property that tells us that objects are initialized

correctly.

As it is common in invariant proofs, we need to strengthen these goals for the proof to succeed. In the previous sections we extended our model by three things: the new types *UnInit* and *PartInit*, the *iheap*, and the reference updates at constructor returns (because we create a new object at each constructor call). It is not surprising that the invariant needs to formally describe the purpose of these extensions. Apart from the relation of the *iheap* to the statically predicted initialization status, this is captured by the following two properties:

- The alias analysis that the BCV does on uninitialized values must be correct. We created the type *UnInit C pc* to keep track of a single value: the reference to the object that was freshly created by the instruction *New C* at address *pc*. Also the type *PartInit C* is intended to keep track of a single value: the *this* pointer in constructors. The predicate *consistent-init stk loc s ihp* (see §A.2) states that each type *UnInit C pc* and *PartInit C* in a state type *s* refers to at most one value in stack and local variables.
- The BCV and the operational semantics must agree on the new objects that are created for constructor calls and on the reference update that we do at constructor returns. The former is part of *correct-frame*, the latter is described by *constructor-ok*. In §A.2, we show both predicates in more detail.

The formal definitions of these additional properties are hard to understand, but we do not need to go into further detail at this point: it is not necessary to understand them in order to trust the proof. It is enough, and more important, to understand the definition of type approximation, since this is the original goal we wanted to prove. The invariant implies that it holds. In the following we show how to extend the notion of type approximation such that it covers object initialization.

The basic building block is single value conformance. A value *v* conforms to a type *T*, if it has the dynamic type *T'* and  $T' \preceq T$ . The function *typeof* :: *aheap* ⇒ *val* ⇒ *ty option* looks up the dynamic type of objects in the heap (for other values, dynamic and static type are always equal):

$$\begin{aligned} (- \vdash - :: \preceq -) &:: \text{aheap} \Rightarrow \text{val} \Rightarrow \text{ty} \Rightarrow \text{bool} \\ \text{hp} \vdash v &:: \preceq T \equiv \exists T'. \text{typeof hp } v = \text{Some } T' \wedge T' \preceq T \end{aligned}$$

We extend it to take the new *iheap* and *init-ty* into account by declaring a new judgment  $\text{hp, ih} \vdash v :: \preceq_i T$ :

$$\begin{aligned}
hp, ih \vdash v :: \preceq_i T &\equiv \\
\text{case } T \text{ of } \text{Init } t &\Rightarrow hp \vdash v :: \preceq t \wedge \text{is-init } hp \text{ ih } v \\
| \text{UnInit } C \text{ pc} &\Rightarrow hp \vdash v :: \preceq \text{Class } C \wedge \\
&\quad \text{typeof } hp \text{ } v = \text{Some } C \wedge \text{tag ih } v = \text{Some } T \\
| \text{PartInit } C &\Rightarrow hp \vdash v :: \preceq \text{Class } C \wedge \text{tag ih } v = \text{Some } T
\end{aligned}$$

For initialized types we just use the existing  $:: \preceq$  and require with *is-init* that the value is initialized. For *UnInit*  $C \text{ pc}$  and *PartInit*  $C$  we require that  $v$  approximates the type *Class*  $C$ , and that the *iheap* exactly agrees with the predicted type (*tag ih v* returns *Some T* if the value  $v$  is an address tagged with type  $T$  in *ih*). In the *UnInit* case we can be more precise: since *UnInit*  $C \text{ pc}$  is only used for freshly created objects, we know that the static type is exact. We call a value  $v$  initialized (*is-init*  $hp \text{ ih } v$ ) if it is not an address, or for addresses that really point to an object, if the *iheap*  $ih$  contains a tag *Init t* for it.

$$\begin{aligned}
\text{tag} &:: \text{iheap} \Rightarrow \text{val} \Rightarrow \text{init-ty option} \\
\text{tag ih } v &\equiv \text{if } \exists l. v = \text{Addr } l \text{ then } \text{Some } (\text{ih } (\text{the-Addr } v)) \text{ else } \text{None}
\end{aligned}$$

$$\begin{aligned}
\text{is-init} &:: \text{aheap} \Rightarrow \text{iheap} \Rightarrow \text{val} \Rightarrow \text{bool} \\
\text{is-init } hp \text{ ih } v &\equiv \forall \text{loc}. v = \text{Addr } \text{loc} \longrightarrow hp \text{ loc} \neq \text{None} \longrightarrow (\exists t. \text{ih } \text{loc} = \text{Init } t)
\end{aligned}$$

Using  $:: \preceq_i$  we now define what it means for a welltyping of stack and local variables to approximate a concrete stack and set of local variables:

$$\begin{aligned}
\text{approx-val} &:: \text{aheap} \Rightarrow \text{iheap} \Rightarrow \text{val} \Rightarrow \text{init-ty err} \Rightarrow \text{bool} \\
\text{approx-val } hp \text{ ih } v \text{ any} &\equiv \text{case any of } \text{Err} \Rightarrow \text{True} \mid \text{OK } T \Rightarrow hp, ih \vdash v :: \preceq_i T
\end{aligned}$$

$$\begin{aligned}
\text{approx-loc} &:: \text{aheap} \Rightarrow \text{iheap} \Rightarrow \text{locvars} \Rightarrow \text{ty err list} \Rightarrow \text{bool} \\
\text{approx-loc } hp \text{ ih } \text{loc } \text{lt} &\equiv \text{list-all2 } (\text{approx-val } hp \text{ ih}) \text{ loc } \text{lt}
\end{aligned}$$

$$\begin{aligned}
\text{approx-stk} &:: \text{aheap} \Rightarrow \text{iheap} \Rightarrow \text{opstack} \Rightarrow \text{ty list} \Rightarrow \text{bool} \\
\text{approx-stk } hp \text{ ih } \text{stk } \text{st} &\equiv \text{approx-loc } hp \text{ ih } \text{stk } (\text{map OK } \text{st})
\end{aligned}$$

In §A.1 the invariant *correct-state* implies that the heap is consistent, i.e. that all objects on the heap only have fields according to their declared type. Now, we also need an *h-init*  $hp \text{ ihp}$  to say that the fields of all objects contain fully initialized values. Both have the same structure: in *h-init* we say that for each object that is defined in the heap  $hp$  and each field  $f$  of these objects, *is-init*  $hp \text{ ihp } f$  must hold. See §A.2 for the formal definition.

These are the properties the invariant must express. The complete invariant is the predicate *correct-state* shown in §A.2. Since it is quite large, complex, and therefore not obvious in its semantics, we need to ask ourselves if that really is what we want prove about the BCV, if it ensures proper object initialization and type safety of bytecode. Fortunately, as already mentioned above, we do

not have to understand the whole invariant to trust it. We proved the following lemma.

**Lemma 16** *If the invariant holds, dynamic types during execution and the initialization status of all objects are correctly approximated by the welltyping:*

$$\begin{aligned} \text{correct-state } (None, hp, ihp, (stk, loc, C, sig, pc, r) \# frs) \Phi \longrightarrow \\ \exists st \, lt \, z. \Phi \, C \, sig = Some \, ((st, lt), z) \wedge \\ \text{approx-stk } hp \, ihp \, stk \, st \wedge \text{approx-loc } hp \, ihp \, loc \, lt \end{aligned}$$

The actual type safety theorem for  $\mu$ Java with object initialization is again:

**Theorem 17** *In a welltyped and wellformed program, execution preserves the invariant:*

$$\text{wt-jvm-prog } \Phi \wedge \text{correct-state } s \, \Phi \wedge s \xrightarrow{\text{jvm}} t \longrightarrow \text{correct-state } t \, \Phi$$

The Isabelle proof of that theorem is about 3100 lines long (not counting lemmas about the type system) and consists mainly of a large case distinction over the instruction set together with a wealth of lemmas about the invariant. The same proof without object initialization only took about 1000 lines. Of these 1000 lines about 600 could be replayed after slight adjustments. The reusable part consisted of lemmas about the invariant (more specifically about its individual parts) and of the general structure of the type safety proof. The detailed reasoning for individual instructions had to be changed due to the stronger properties to prove. The additional work is mostly due to the new parts of the invariant adding not only in size but also in complexity. Especially the alias analysis predicate *consistent-init* causes an increase in proof size: since it is designed to keep track of single values, each kind of instruction required its own set of lemmas (which was not necessary for the rest of the invariant).

Freund presents a similar proof of type safety in his PhD thesis [23]. While Freund's model as a whole is sound and at least the theorems we have looked at more closely all hold, it still contains some subtle problems (as the non monotonicity of the typing rule for *New*) as well as small errors in the proofs (for instance an incomplete case distinction in lemma D.16.16). We are not trying to find fault in Freund's thesis, however: for a formal development of this size done by hand it is of remarkably high quality. Rather, the point is that exactly in a large and complex formal development there are bound to be human errors, and these are addressed by theorem provers like Isabelle. Isabelle not only does not allow you to make errors in proofs, its automatic tactics also have become powerful enough to help with the technical detail. Another effect of a strict, mechanical formal development is that specifications get validated more thoroughly: a single non monotone rule in the specification is easy to overlook and skip over and thus might not turn up as a problem

in hand proofs about the specification. In a theorem prover these kinds of inconsistencies turn up quickly. They also can be fixed more easily and safely because it is easy to see which properties are still valid and which are affected by the change.

## 6 Conclusion

By verifying an executable BCV, this work closes a significant gap in the effort to provide a machine-checked formalization of the Java/JVM architecture. Despite its relative compactness (the abstract executable BCV is about 2000 lines of specifications, programs, and proofs), the amount of work to construct such a detailed model should not be underestimated. But when it comes to security, there is no substitute for complete formality and tools like Isabelle are very well suited to support it.

We have demonstrated that our abstract formalization of the executable BCV integrates well with our existing work about Java and the JVM. As promised in [21] we included a more realistic feature, object initialization, in our model. We proved that the BCV framework copes well with such significant changes in the type system ( $\mu$ Java was about 9000 lines of Isabelle code before and 11500 lines after adding object initialization).

Including exception handling in the formalization is not difficult; in fact we have already done so [24], but leave it out here for clarity. The complication with exceptions is that two successors of the same instruction can have different result state types in the dataflow analysis (one for normal execution, a different one when an exception occurs). The idea is to change the type of the transfer function such that it yields a list of pairs each consisting of successor instruction and state type.

The JSR instruction is the next challenge on our agenda.

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## A Conformance Relation

### A.1 Conformance without object initialization

This section shows the formal definition of the conformance relation between dynamic JVM states and static types in the  $\mu$ Java model without object initialization. It is explained in detail in [2] and [3], so we will be brief here.

A heap conforms if all objects conform. An object conforms if all fields conform to their declared type. The function *fields* takes a program and a class and yields a function that maps field names to their declared type. For the definition of single value conformance  $hp \vdash v ::\preceq T$  see §5.4, p. 38.

$$\begin{aligned} lconf &:: aheap \Rightarrow (\alpha \Rightarrow val\ option) \Rightarrow (\alpha \Rightarrow ty\ option) \Rightarrow bool \\ lconf\ hp\ vs\ Ts &\equiv \\ \forall n\ T. Ts\ n = Some\ T &\longrightarrow (\exists v. vs\ n = Some\ v \wedge hp \vdash v ::\preceq T) \end{aligned}$$

$$\begin{aligned} oconf &:: aheap \Rightarrow obj \Rightarrow bool \\ oconf\ hp\ (C,fs) &\equiv lconf\ hp\ fs\ (fields\ (\Gamma,C)) \end{aligned}$$

$$\begin{aligned} (-\ \checkmark) &:: aheap \Rightarrow bool \\ hp\ \checkmark &\equiv \forall a\ obj. hp\ a = Some\ obj \longrightarrow oconf\ hp\ obj \end{aligned}$$

Single value conformance is lifted to a register set and stack by *approx-loc* and *approx-stk*. Any value conforms to the unusable type *Err*.

$$\begin{aligned} approx\text{-}val &:: aheap \Rightarrow val \Rightarrow init\text{-}ty\ err \Rightarrow bool \\ approx\text{-}val\ hp\ v\ any &\equiv case\ any\ of\ Err \Rightarrow True \mid OK\ T \Rightarrow hp \vdash v ::\preceq T \end{aligned}$$

$$\begin{aligned} approx\text{-}loc &:: aheap \Rightarrow locvars \Rightarrow ty\ err\ list \Rightarrow bool \\ approx\text{-}loc\ hp\ loc\ lt &\equiv list\text{-}all2\ (approx\text{-}val\ hp)\ loc\ lt \end{aligned}$$

$$\begin{aligned} approx\text{-}stk &:: aheap \Rightarrow opstack \Rightarrow ty\ list \Rightarrow bool \\ approx\text{-}stk\ hp\ stk\ st &\equiv approx\text{-}loc\ hp\ stk\ (map\ OK\ st) \end{aligned}$$

A call frame conforms, if its stack and register set conform, the program counter lies inside the instruction list, and if the register set has space for the *this* pointer, the method parameters, and the local variables.

$$\begin{aligned} correct\text{-}frame &:: aheap \Rightarrow state\text{-}type \Rightarrow nat \Rightarrow instr\ list \Rightarrow frame \Rightarrow bool \\ correct\text{-}frame\ hp\ (st,lt)\ m\!x\!l\ ins\ (stk,loc,C,sig,pc) &\equiv \\ &approx\text{-}stk\ hp\ stk\ st \wedge approx\text{-}loc\ hp\ loc\ lt \wedge \\ &pc < size\ ins \wedge size\ loc = size\ (snd\ sig) + m\!x\!l + 1 \end{aligned}$$

The following definition uses  $prog\text{-}type = cname \Rightarrow sig \Rightarrow method\text{-}type$  which

lifts *method-type* to whole programs. The predicate describes the structure of the call frame stack beneath the topmost frame. The parameters  $rt_0$  and  $sig_0$  are the return type and signature of the topmost frame. A list of call frames conforms if it is empty. If it is not empty, the head frame is investigated more closely: the current state type  $\Phi C sig ! pc$  must denote a reachable instruction; the call frame must belong to a defined method; it must be halted at an *Invoke* instruction which created the call frame above (this is not easily expressed, but we can demand that  $mn$  and  $ps$  stem from  $sig_0$ , that the return type of a static lookup on  $C'$  conforms to the one from the frame above ( $rt_0$ ), and that the current stack is large enough to hold the actual parameters plus the object on which the method was invoked); finally the current frame and the rest of the call frame stack must conform.

```

correct-frames :: aheap  $\Rightarrow$  prog-type  $\Rightarrow$  ty  $\Rightarrow$  sig  $\Rightarrow$  frame list  $\Rightarrow$  bool
correct-frames hp  $\Phi$   $rt_0$   $sig_0$  [] = True
correct-frames hp  $\Phi$   $rt_0$   $sig_0$  (f#frs) =
let (stk,loc,C,sig,pc) = f; (mn,ps) = sig_0 in
   $\exists st\ lt\ rt\ mxs\ mxl\ ins\ C'$ .
     $\Phi\ C\ sig\ !\ pc = Some\ (st,lt) \wedge is-class\ \Gamma\ C \wedge$ 
    method  $(\Gamma,C)\ sig = Some(C,rt,mxs,mxl,ins) \wedge$ 
    ins!pc = Invoke  $C'\ mn\ ps \wedge$ 
    ( $\exists D'\ rt'\ b'. method\ (\Gamma,C')\ sig_0 = Some(D',rt',b') \wedge rt_0 \preceq rt'$ )  $\wedge$ 
    ( $\exists as\ t\ st'. st = (rev\ as)@t\ \#st' \wedge size\ as = size\ ps \wedge$ 
      correct-frame hp (st,lt) mxl ins f  $\wedge$ 
      correct-frames hp  $\Phi\ rt\ sig\ frs$ )

```

The following is the toplevel conformance relation between a state and a program type. The first two cases are trivial, the third case requires a conformant heap ( $hp \checkmark$ ), contains special handling for the topmost call frame and delegates the rest to *correct-frames*. The topmost frame is special because it does not need to be halted at an *Invoke* instruction. The topmost frame must conform and the current state type must denote a reachable instruction. The method lookup provides *correct-frame* and *correct-frames* with the required parameters.

```

correct-state :: jvm-state  $\Rightarrow$  prog-type  $\Rightarrow$  bool
correct-state (Some xp, hp, frs)  $\Phi = True$ 
correct-state (None, hp, [])  $\Phi = True$ 
correct-state (None, hp, f#fs)  $\Phi =$ 
let (stk,loc,C,sig,pc) = f in
   $\exists rt\ mxs\ mxl\ ins\ s.$ 
    method  $(\Gamma,C)\ sig = Some(C,rt,mxs,mxl,ins) \wedge \Phi\ C\ sig\ !\ pc = Some\ s \wedge$ 
    correct-frame hp s mxl ins f  $\wedge$  correct-frames hp  $\Phi\ rt\ sig\ fs \wedge$ 
    hp  $\checkmark \wedge is-class\ \Gamma\ C$ 

```

## A.2 Conformance with object initialization

This section shows the full definition of the conformance relation as far as it not already appeared in §5.4.

We begin with the definition of *h-init*: all objects only have fields with initialized values. The structure is analogous to heap conformance (see §A.1):

$$\begin{aligned} l\text{-init} &:: \text{aheap} \Rightarrow \text{iheap} \Rightarrow (\alpha \Rightarrow \text{val option}) \Rightarrow (\alpha \Rightarrow \text{ty option}) \Rightarrow \text{bool} \\ l\text{-init hp ih vs Ts} &\equiv \\ \forall n T. Ts n = \text{Some } T &\longrightarrow (\exists v. vs n = \text{Some } v \wedge \text{is-init hp ih } v) \end{aligned}$$

$$\begin{aligned} o\text{-init} &:: \text{aheap} \Rightarrow \text{iheap} \Rightarrow \text{obj} \Rightarrow \text{bool} \\ o\text{-init hp ih } (C, fs) &\equiv l\text{-init hp ih } fs \text{ (fields } (\Gamma, C)) \end{aligned}$$

$$\begin{aligned} h\text{-init} &:: \text{aheap} \Rightarrow \text{iheap} \Rightarrow \text{bool} \\ h\text{-init hp ih} &\equiv \forall a \text{ obj. hp } a = \text{Some obj} \longrightarrow o\text{-init hp ih obj} \end{aligned}$$

The next component of the conformance relation regards the alias analysis on uninitialized objects. This part follows closely the description in [23].

The basic idea of the judgment *consistent-init* is: if the static local variables or the stack (on the type level) contain two entries *UnInit C pc* then the corresponding entries in the dynamic local variables and stack (on the value level) must contain the same value. Otherwise the BCV might mistakenly mark uninitialized values as initialized at *Invoke-spl*. The other direction does not hold necessarily: if two values are equal, they do not need to have the same type (one could be *UnInit C pc* the other *Err*). We also require that all uninitialized values are tagged correctly in the *ihp*.

$$\begin{aligned} \text{corr-loc} &:: \text{locvars} \Rightarrow \text{ty err list} \Rightarrow \text{iheap} \Rightarrow \text{val} \Rightarrow \text{init-ty} \Rightarrow \text{bool} \\ \text{corr-loc loc lt ihp } v T &\equiv \\ \text{list-all2 } (\lambda l t. t = \text{OK } T &\longrightarrow l = v \wedge \text{tag ihp } v = \text{Some } T) \text{ loc lt} \end{aligned}$$

$$\begin{aligned} \text{corr-stk} &:: \text{opstack} \Rightarrow \text{ty list} \Rightarrow \text{iheap} \Rightarrow \text{val} \Rightarrow \text{init-ty} \Rightarrow \text{bool} \\ \text{corr-stk stk st ihp } v T &\equiv \text{corr-loc stk (map OK st) ihp } v T \end{aligned}$$

$$\begin{aligned} \text{corresponds} &:: \text{opstack} \Rightarrow \text{locvars} \Rightarrow \text{state-type} \Rightarrow \text{iheap} \Rightarrow \text{val} \Rightarrow \text{init-ty} \Rightarrow \text{bool} \\ \text{corresponds stk loc s ihp } v T &\equiv \\ \text{corr-stk stk (fst s) ihp } v T &\wedge \text{corr-loc loc (snd s) ihp } v T \end{aligned}$$

$$\begin{aligned} \text{consistent-init} &:: \text{opstack} \Rightarrow \text{locvars} \Rightarrow \text{state-type} \Rightarrow \text{iheap} \Rightarrow \text{bool} \\ \text{consistent-init stk loc s ihp} &\equiv \\ (\forall C pc. \exists v. \text{corresponds stk loc s ihp } v &(\text{UnInit } C pc) ) \wedge \\ (\forall C. \exists v. \text{corresponds stk loc s ihp } v &(\text{PartInit } C) ) \end{aligned}$$

With these definitions we can define conformance of call frames as follows: stack and register set conform, the alias analysis is correct, the type *PartInit* is only used for the *this* pointer in constructors, the *this* pointer in constructors is tagged correctly, the *pc* is inside the method, and the size of the register set is correct:

$$\begin{aligned}
\text{correct-frame} &:: \text{aheap} \Rightarrow \text{iheap} \Rightarrow \text{state-type} \Rightarrow \text{nat} \Rightarrow \text{instr list} \Rightarrow \text{frame} \Rightarrow \text{bool} \\
\text{correct-frame } hp \text{ ih } (st, lt) \text{ m}\times\text{l ins } (stk, loc, C, sig, pc, (this, c)) &\equiv \\
&\text{approx-stk } hp \text{ ih } stk \text{ st} \wedge \text{approx-loc } hp \text{ ih } loc \text{ lt} \wedge \\
&\text{consistent-init } stk \text{ loc } (st, lt) \text{ ih} \wedge \\
&(\text{fst } sig = \text{init} \longrightarrow \\
&\quad \text{corresponds } stk \text{ loc } (st, lt) \text{ ih } this \text{ (PartInit } C) \wedge \\
&\quad (\exists C'. \text{typeof } hp \text{ this} = \text{Some } C' \wedge \\
&\quad \quad \text{tag ih this} \in \{\text{Some (PartInit } C), \text{Some (Init (Class } C'))\}) \wedge \\
&\quad pc < \text{size ins} \wedge \text{size loc} = \text{size(snd sig)} + \text{m}\times\text{l} + 1
\end{aligned}$$

The predicate *constructor-ok* describes the stored references in constructor call chains. It does not describe the situation completely, though. Only if we take into account that the program is welltyped, i.e. that *app* holds for every instruction, do we get the whole picture. To achieve this, *constructor-ok* relates the following three references:

- The *this* pointer *a* of the calling constructor (of frame *n*). That is the reference originally to be initialized. Since at that point the initialization process cannot be complete, it must have the *iheap* tag *UnInit* or *PartInit*.
- The *this* pointer *b* of the current constructor (of frame *n + 1*). This is the reference to the object that was artificially created for this constructor call. It is one step further in the initialization chain and must therefore be tagged with *PartInit* or *Init* (*Init*, only if we have reached the end of the chain and arrived at *Object*). We require that if the tag is *PartInit* then it must be *PartInit C* where *C* is the current class, and if it is some *Init (Class D)* then *D* from the *iheap* must be equal to the dynamic type *C'* from the heap. That *b* is exactly one step further and only initialized if the current class is *Object* can be inferred from *app* and the start value of the BCV.
- The reference *c* to the fully initialized object. It will be passed up along the initialization chain by the *Return* instructions in constructors (see also the semantics of the *Return* instruction in §5.2). The reference can be *Null* when the superclass constructor has not been called yet. More specifically it will exactly be *Null* as long as the superclass-constructor-has-been-called marker *z* of the BCV is false. If it is not *Null* then it must point to an initialized object of type *C'*.

All three references have to agree on the dynamic type *C'* of the object (since it is the same object copied around). Formally this lengthy text reads as:

$$\text{constructor-ok} :: \text{aheap} \Rightarrow \text{iheap} \Rightarrow \text{val} \Rightarrow \text{cname} \Rightarrow \text{bool} \Rightarrow \text{val} \times \text{val} \Rightarrow \text{bool}$$

$constructor-ok\ hp\ ih\ a\ C\ z\ (b, c) \equiv$   
 $\exists C' D\ pc. z = (c \neq Null) \wedge$   
 $typeof\ hp\ a = Some\ C' \wedge typeof\ hp\ b = Some\ C' \wedge$   
 $(c \neq Null \longrightarrow typeof\ hp\ c = Some\ C') \wedge$   
 $tag\ ih\ a \in \{Some\ (UnInit\ C'\ pc), Some\ (PartInit\ D)\} \wedge$   
 $tag\ ih\ b \in \{Some\ (PartInit\ C), Some\ (Init\ (Class\ C'))\} \wedge$   
 $(c \neq Null \longrightarrow tag\ ih\ c = Some\ (Init\ (Class\ C')))$

The rest of the invariant proceeds much as in §A.1, we merely have inserted *constructor-ok* and adjusted for *Invoke-special*. The parameters  $rt_0$ ,  $sig_0$ ,  $z_0$ ,  $r_0$  stem from the call frame above the current  $f$ :

$correct-frames :: aheap \Rightarrow iheap \Rightarrow prog-type \Rightarrow ty \Rightarrow sig \Rightarrow bool \Rightarrow$   
 $val \times val \Rightarrow frame\ list \Rightarrow bool$   
 $correct-frames\ hp\ ih\ \Phi\ rt_0\ sig_0\ z_0\ r_0\ [] = True$   
 $correct-frames\ hp\ ih\ \Phi\ rt_0\ sig_0\ z_0\ r_0\ (f\#\#frs) =$   
 $let\ (stk, loc, C, sig, pc, r) = f; (mn, ps) = sig_0\ in$   
 $\exists st\ lt\ z\ rt\ mxs\ mxl\ ins\ C'.$   
 $\Phi\ C\ sig\ !\ pc = Some\ ((st, lt), z) \wedge is-class\ \Gamma\ C \wedge$   
 $method\ (\Gamma, C)\ sig = Some(C, rt, mxs, mxl, ins) \wedge$   
 $ins!pc \in \{Invoke\ C'\ mn\ ps, Invoke-special\ C'\ mn\ ps\} \wedge$   
 $(\exists D'\ rt'\ b'. method\ (\Gamma, C')\ sig_0 = Some(D', rt', b') \wedge rt_0 \preceq rt') \wedge$   
 $(\exists as\ t\ st'. st = (rev\ as)@t\#\#st' \wedge size\ as = size\ ps \wedge$   
 $(mn = init \longrightarrow constructor-ok\ hp\ ih\ (stk!\ size\ as)\ C'\ z_0\ r_0) \wedge$   
 $correct-frame\ hp\ ih\ (st, lt)\ mxl\ ins\ f \wedge$   
 $correct-frames\ hp\ ih\ \Phi\ rt\ sig\ z\ r\ frs)$

To the toplevel conformance predicate we add the new *h-init*, the rest of the definition is the same as in §A.1:

$correct-state :: jvm-state \Rightarrow prog-type \Rightarrow bool$   
 $correct-state\ (Some\ xp, hp, ihp, frs)\ \Phi = True$   
 $correct-state\ (None, hp, ihp, [])\ \Phi = True$   
 $correct-state\ (None, hp, ihp, f\#\#fs)\ \Phi =$   
 $let\ (stk, loc, C, sig, pc, r) = f\ in$   
 $\exists rt\ mxs\ mxl\ ins\ s\ z.$   
 $method\ (\Gamma, C)\ sig = Some(C, rt, mxs, mxl, ins) \wedge \Phi\ C\ sig\ !\ pc = Some\ (s, z) \wedge$   
 $correct-frame\ hp\ ihp\ s\ mxl\ ins\ f \wedge correct-frames\ hp\ ihp\ \Phi\ rt\ sig\ z\ r\ fs \wedge$   
 $\Gamma \vdash h\ hp\ \surd \wedge h-init\ hp\ ihp \wedge is-class\ \Gamma\ C$

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